DATA MODUL

ORTUSTECH

Specification

COM27H2R06ULC

2,7" - 320 x 240 - RGB - Blanview

Spec Revision: 1.0 Revision Date: 27.11.2023

Note: This specification is subject to change without prior notice

Passion Displayed

SPECIFICATIONS № 23TLM020	(1/40) Issue:Nov.27,2023
A SPECIFICATIONS № 231LM020	13545.1109.27,2023
Specifications for	
Blanview TFT-LCD Monitor	
(2.7" QVGA 240 x RGB x 320 Portrait)	
Version 1.0	
(Please be sure to check the specifications latest version.)	
MODEL COM27H2R06ULC	
Customer's Approval	
Signature :	
Name :	
Section :	
Title :	
Date :	
ORTUSTECH	
Electronics Division	
Technological Develop	ment Department III
Approved by	En l'
	guan
Checked by	
<i>..M</i>	atsumak e
Prepared by	2 - 1 `
	n. 2010
	U
TOPPAN INC.	

Version History

Ver. Date Page Description											
0.0	0.0 Jul.24,2023 Tentative issue										
1.0	Nov.27,2023	-	-	First issue							
۵		All		All							
∕A ×12			Change	Company name logo font							
		P.5		2.1 Features of the Product							
			Delete	Error delete							
		P.7		3.1 Dimensions							
	Correct Weight										
		P.8	. .	3.2 Outward Form							
		D 40	Correct	Error correct							
		P.13	A	7.2.1 3-wire serial interface timing characteristics							
		P.14	Correct	Rating							
		P.14	Corroct	7.2.2 4-wire serial interface timing characteristics							
		P.20,21	Correct	Rating 9.1 Power ON Sequence							
		F.20,21	Correct	Register value							
		P.23,24		9.5 Refresh Sequence							
		1.20,24	Correct	Register value							
		P.29	Joneou	12.1 Defective Display and Screen Quality							
			Correct	Signal condition							
			Correct	Error correct							
		P.31		13. Reliability Test							
			Add	number of failures /number of examinations							
				TOPPAN INC.							

Contents

1.	Applica	ation	••••	4
2.	Outline	e Specifications		
	2.1 F	Features of the Product	••••	5
	2.2 [Display Method	••••	5
3.	Dimen	sions and Shape		
	3.1 [Dimensions	••••	7
	3.2 (Dutward Form	••••	8
	3.3 \$	Serial Label (S-Label)	• • • • • • • • • •	9
4.	Pin As	ssignment	• • • • • • • • • •	10
		ute Maximum Rating	• • • • • • • • • •	11
		nmended Operating Conditions	• • • • • • • • • •	11
		cteristics		
	7.1	DC Characteristics	••••	12
	7.2	AC Characteristics	••••	13
8.	Interfa			
-		nterface		15
		Write protocol		16
		Read protocol		17
		Data transfer		18
g	Seque			10
0.	-	Power ON Sequence		20
		Sleep IN Sequence		22
		Sleep OUT Sequence		22
		Power OFF Sequence		22
		Refresh Sequence		22
		-		25
10		Power ON/OFF timing		
	LED (26
11.	-			07
		Optical Characteristics	•••••	27
		Temperature Characteristics	••••	28
12.		a of Judgment		
		Defective Display and Screen Quality	••••	29
		Screen and Other Appearance	•••••	30
		pility Test	••••	31
		ng Specifications	• • • • • • • • •	33
15.		ing Instruction		
		Cautions for Handling LCD panels	••••	34
		Precautions for Handling	••••	35
	15.3 F	Precautions for Operation	••••	35
		Storage Condition for Shipping Cartons	••••	36
	15.5	Precautions for Peeling off		
		the Protective film	• • • • • • • • • •	37
	15.6 \	Warranty	••••	37
A -				
AH	PPEND	IΛ	• • • • • • • • •	38

1. Application

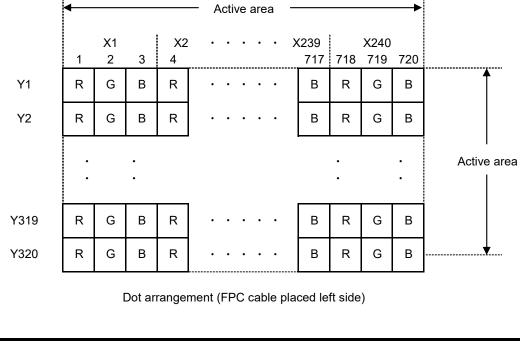
This Specification is applicable to 68.4 mm (2.7 inch) Blanview TFT-LCD monitor for non-military use.

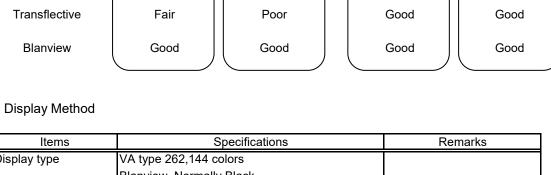
- O TOPPAN makes no warranty or assume no liability that use of this Product and/or any information including drawings in this Specification by Purchaser is not infringing any patent or other intellectual property rights owned by third parties, and TOPPAN shall not grant to Purchaser any right to use any patent or other intellectual property rights owned by third parties, where any patent or other intellectual property rights owned by third parties. Since this Specification contains TOPPAN's confidential information and copy right, Purchaser shall use them with high degree of care to prevent any unauthorized use, disclosure, duplication, publication or dissemination of TOPPAN's confidential information and copy right.
- If Purchaser intends to use this Products for an application which requires higher level of reliability and/or safety in functionality and/or accuracy such as transport equipment (aircraft, train, automobile, etc.), disaster-prevention/security equipment or various safety equipment, Purchaser shall consult TOPPAN on such use in advance.
- This Product shall not be used for application which requires extremely higher level of reliability and/or safety such as aerospace equipment, telecommunication equipment for trunk lines, control equipment for nuclear facilities or life-support medical equipment.
- ◎ It must be noted as an mechanical design manner, especial attention in housing design to prevent arcuation/flexure caused by stress to the LCD module shall be considered.
- O TOPPAN assumes no liability for any damage resulting from misuse, abuse, and/or miss-operation of the Product deviating from the operating conditions and precautions described in the Specification.
- It shall be mutually conferred if nonconforming defect which result from unspecified cause in this specification arises.
- ◎ If any issue arises as to information provided in this Specification or any other information, TOPPAN and Purchaser shall discuss them in good faith and seek solution.
- O TOPPAN assumes no liability for defects such as electrostatic discharge failure occurred during peeling off the protective film or Purchaser's assembly process.

◎ This Product is compatible for RoHS(2.0) directive.

Object substance	Maximum content [ppm]
Cadmium and its compound	100
Hexavalent Chromium Compound	1000
Lead & Lead compound	1000
Mercury & Mercury compound	1000
Polybrominated biphenyl series (PBB series)	1000
Polybrominated biphenyl ether series (PBDE series)	1000
Bis(2-ethylhexyl)phthalate series(DEHP series)	1000
Butyl benzyl phthalate series(BBP series)	1000
Dibutyl phthalate series(DBP series)	1000
Diisobutyl phthalate series(DIBP series)	1000

TOPPAN INC.





Power Efficiency

(Battery Life)

Good

2.2 Display Method

Transmissive

Items	Specifications	Remarks
Display type	VA type 262,144 colors	
	Blanview, Normally Black	
Driving method	a-Si TFT Active matrix	
	Line-scanning, Non-interlace	
Dot arrangement	RGB stripe arrangement	Refer to "Dot arrangement"
Signal input method	3-wire, 4-wire serial interface	
Backlight type	Long life & High bright white LED	
NTSC ratio	50%	

A 2.1 Features of the Product

2. Outline Specifications

- 2.7 inch diagonal display, 720 [H] x 320 [V] dots. 240RGB x 320 pixel.
- 18-bit / 262,144 colors.
- Timing generator [TG], Counter-electrode driving circuitry, Built-in power supply circuit.

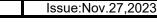
Indoor

SPECIFICATIONS № 23TLM020

- Long life & High bright white LED back-light.
- Blanview TFT-LCD, improved outdoor visibility.

Readability

Good



Outdoor

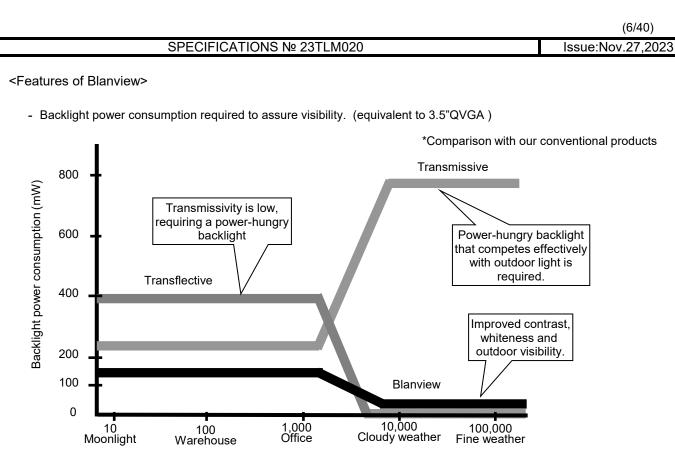
Power Efficiency

(Battery Life)

Poor

Readability

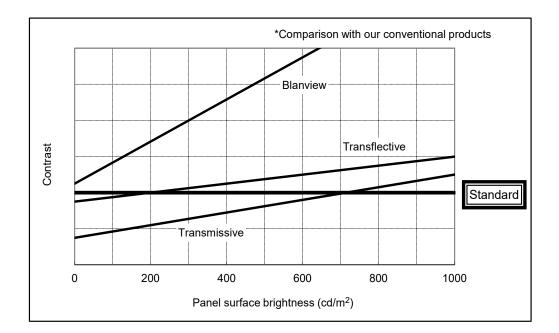
Fair



Surrounding illumination (Ix)

- Contrast characteristics under 100,000lx. (same condition as direct sunlight.) With better contrast (higher contrast ratio), Blanview TFT-LCD has the best outdoor readability in three different types of TFT-LCD.

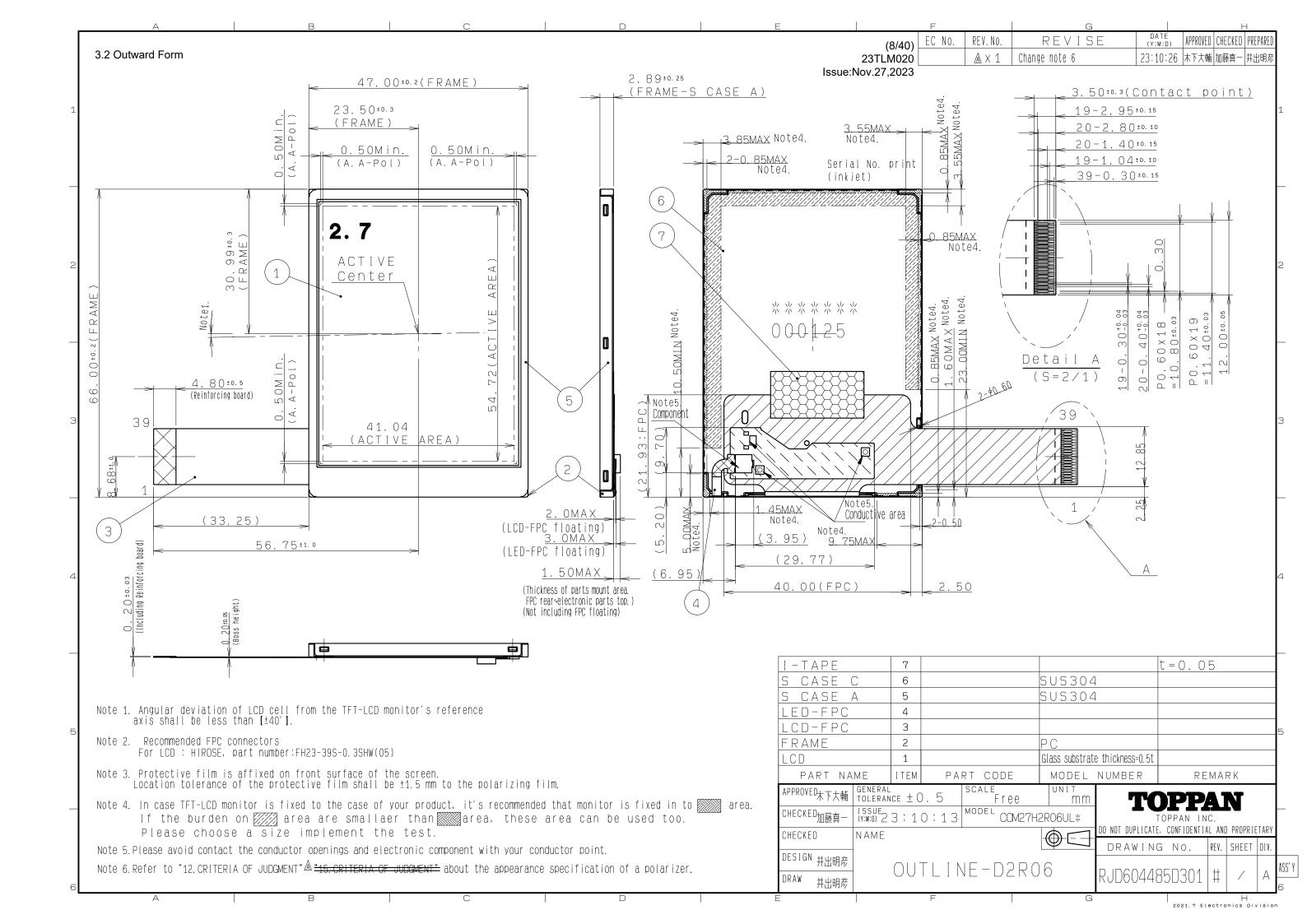
Below chart shows contrast value against panel surface brightness. (Horizontal: Panel surface brightness/ Vertical: Contrast value) LCD panel has enough outdoor readability above our Standard line. (TOPPAN criteria)



3. Dimensions and Shape

A 3.1 Dimensions

Items Specifications		Unit	Remarks
Outline dimensions	47.00[H] × 66.00[V] × 2.89[D]	mm	exclude FPC and components on the FPC
Active area	41.04[H] × 54.72[V]	mm	68.4mm diagonal
Number of dots	720[H] × 320[V]	dot	
Dot pitch	57.0[H] × 171.0[V]	um	
Surface hardness of the	2	Н	
polaraizer			
Weight	18.0	g	Include FPC cable



3.3 Serial № print (S-print)

3.3.1 Display Items

S-print indicates the least significant digit of manufacture year (1digit), manufacture month with below alphabet (1letter), model code (5characters), serial number (6digits).

* Contents of Display

<u>* * *****</u> <u>******</u> a b c d

	Contents of display								
а	The least significant d	igit of manufacture year	r						
b	Manufacture month	Jan-A	May-E	Sep-I					
		Feb-B Jun-F Oct-J							
		Mar-C Jul-G Nov-K							
		Apr-D Aug-H Dec-L							
С	Model code 27HFC (Made in Japan)								
	27HGC (Made in Malaysia)								
d	Serial number								

* Example of indication of Serial № print (S-print)

Made in Japan

3L27HFC000125

means "manufactured in December 2023, 2.7 inch, HF type, C specifications, serial number 000125"

Made in Malaysia

3L27HGC000125

means "manufactured in December 2023, 2.7 inch, HG type, C specifications, serial number 000125"

3.3.2 Location of Serial № print (S-print) Refer to 3.2 "Outward Form".

3.3.3 Others

Please note that it is likely to disappear with an organic solvent about the Serial print.

4. Pin Assignment

No.	Symbol	Function						
1	VSS	GND						
2	VCI	Power supply for main circuit						
3	IOVCC	Power supply for I/O circuit						
4	TE	Tearing effect signal. If not used, please this pin open.						
5	SDO	Serial interface output pin. If not used, please this pin open.						
6	DB15	Parallel data input						
7	DB14	Unused pin. Please fix to GND level.						
8	DB13							
9	DB12							
10	DB11							
11	DB10							
12	DB9							
13	DB8]						
14	DB7							
15	DB6							
16	DB5							
17	DB4							
18	DB3							
19	DB2							
20	DB1							
21	DB0							
22	VSS	GND						
23	SDA	Serial interface data I/O.						
24	RDX	Unused pin. Please fix to GND level.						
25	WRX	Register selection signal (for 4-wire I/F). If not used, please fix to GND level.						
26	TEST	Test pin. Please keep this pin open						
27	DCX	Serial interface clock.						
28	CSX	Chip selection signal (Lo : Select, Hi : Unselect)						
29	RESX	Reset signal (Lo-active)						
30	IM3	Interface mode setting pin. 3-wire serial I : IM[3:0]=0101						
31	IM2	4-wire serial I : IM[3:0]=0110						
32	IM1	3-wire serial II : IM[3:0]=1101						
33	IM0	4-wire serial II : IM[3:0]=1110						
34	NC	Open						
35	NC	Open						
36	NC	Open						
37	NC	Open						
38	BLH	LED drive power source. (Anode side)						
39	BLL	LED drive power source. (Cathode side)						

Note :

- Recommended connector : Hirose FH23 series "FH23-39S-0.3SHW(05) "
- In the circuit design, the terminal array of connector for use with terminal sequence of the "3.2 Outward Form", please be sure to check.
- If the array of the signal input to the product is different, it may cause a malfunction.
- FPC of the terminal has been decorated with gold-plated.

Connector contact terminals is recommended the use of gold-plated products.

5. Absolute Maximum Rating

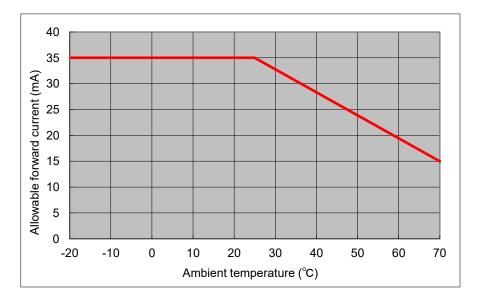
VSS=	<u>۱۱/</u>
V00-	UV.

ltem	Symbol	Condition	Rating		Unit	Applicable terminal
			MIN	MAX		
Supply voltage	VCI		-0.3	4.6	V	VCI
Supply voltage	IOVCC		-0.3	VCI	V	IOVCC
Input voltage for logic	VI		-0.3	IOVCC+0.3	V	SDA,RDX,WRX,DCX,CSX,
						RESX,IM[3:0]
LED Forward current	IL	Ta=25°C	—	35.0	mA	BLH - BLL
		Ta=70°C	—	15.0		
Storage temperature range	Tstg		-30	80	°C	
Storage atmospheruc range	Hstg	40°C90%RH o with no conde	H or less of moisture content densation			

6. Recommended Operating Conditions

	-						VSS=0V
Item	Symbol	Condition		Rating		Unit	Applicable terminal
			MIN	TYP	MAX		
Supply voltage	VCI		2.8	3.3	3.6	V	VCI
Supply voltage	IOVCC		1.8	VCI	VCI	V	IOVCC
Input voltage for logic	VI		0	_	IOVCC	V	SDA,RDX,WRX,DCX,CSX, RESX,IM[3:0]
Operational temperature range	Тор	*note	-20	25	70	°C	LCD Panel surface temperature
Operating humidity	Нор	Ta≦40°C	20	_	85	%	
range		Ta> 40°C	40°C85%RH or less of moisture content with no condensation				

note : The maximum value of LED Forward current "IL", do not exceed the following allowable current value.



7. Characteristics

7.1 DC Characteristics

7.1.1 Display section

(Unless otherwise noted, Ta=25°C,VCI=3.3V,IOVC									
Item	Symbol	Condition		Rating		Unit	Applicable terminal		
			MIN	TYP	MAX				
Input Signal	VIH		0.7×IOVCC		IOVCC	V	SDA,RDX,WRX,DCX,CSX		
Voltage	VIL		0	—	0.3×IOVCC	V	RESX,IM[3:0]		
Output Signal	VOH	IOH = -0.1mA	0.8×IOVCC	—	—	V	D0-D17,TE		
Voltage	VOL	IOL = 0.1mA	—	—	0.2×IOVCC	V			
Operating	ICI		—	6.5	13	mA	VCI		
Current	IOICC	Color bar *note	—	5	10	uA	IOVCC		
Standby	ICI		_	15	30	uA	VCI		
Current	IOICC		_	5	10	uA	IOVCC		

note : CPU is not accessing the display RAM, still image display state (Color bar display)

7.1.2 Backlight section

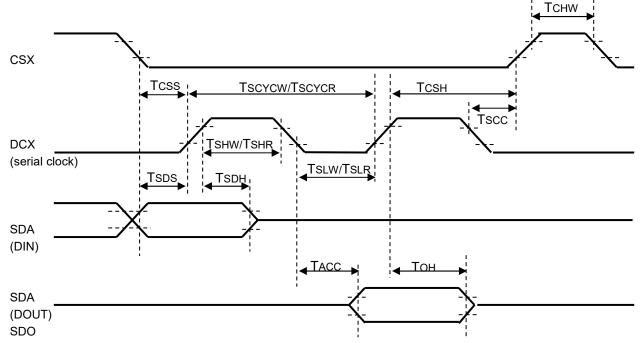
Item	Symbol	Condition	Rating			Unit	Applicable terminal
			MIN	TYP	MAX		
Forward	IL25	Ta=25℃	—	7.0	35.0	mA	BLH - BLL
current	IL70	Ta=70℃	—	—	15.0	mA	
Forward	VL	Ta=25℃, IL=7.0mA	_	8.0	8.5	V	
voltage							
Estimated	LL	Ta=25°C, IL=7.0mA	_	50,000	_	hrs	
Life of LED		Note					

note :

- The lifetime of the LED is defined as a period till the brightness of the LED decreases to the half of its initial value.
- This figure is given as a reference purpose only, and not as a guarantee.
- This figure is estimated for an LED operating alone. As the performance of an LED may differ when assembled as a monitor.
- Estimated lifetime could vary on a different temperature and usually higher temperature could reduce the life significantly.

7.2 AC Characteristics

A 7.2.1 3-wire serial interface timing characteristics



Item	Symbol	Ra	ting	Unit			
nem	Symbol	MIN	MAX	Onit			
Chip Select Setup Time (Write)	TCSS	15		ns	CSX		
Chip Select Hold Time (Write)	TCSH	15		ns			
Chip Select Setup Time (Read)	TCSS	60		ns	1		
Chip Select Hold Time (Read)	TSCC	65		ns	1		
Chip Select "H" Pulse Width (Write)	TCHW	40		ns	1		
Serial Clock Cycle (Write)	TSCYCW	50		ns	DCX		
Serial Clock "H" Pulse Width (Write)	TSHW	7		ns	1		
Serial Clock "L" Pulse Width (Write)	TSLW	7		ns	1		
Serial Clock Cycle (Read)	TSCYCR	150		ns	1		
Serial Clock "H" Pulse Width (Read)	TSHR	60		ns	1		
Serial Clock "L" Pulse Width (Read)	TSLR	60		ns			
Data Setup Time	TSDS	7		ns	SDA		
Data Hold Time	TSDH	7		ns	1		
Access Time	TACC	10	50	ns	SDA(DOUT)/SDO		
Output disable Time	ТОН	15	50	ns	1		
Input signal rise time	tr		15	ns			
Input signal fall time	tf		15	ns			

(Unless otherwise noted, Ta=25°C,VCI=3.3V,IOVCC=3.3V,VSS=0V)

Note: All timing is defined as the reference to the 30-70% of IOVCC.

					(14/40)
SPECIFICA	TIONS № 2	23TLM020			Issue:Nov.27,20
A 7.2.2 4-wire serial interface timing chara	acteristics				Тсни
	Тѕсүс	W/TSCYCR		TCSI	
DCX (serial clock)		TSLV	v/Tslr	-	
WRX (command/parameter)					
SDA					
SDA (DOUT) SDO		- TAC		Тон	
	(Unless	otherwise	noted, Ta=2	•	=3.3V,IOVCC=3.3V,VSS=0V)
(DOUT)	(Unless Symbol	i ● s otherwise Ra	noted, Ta=2	•	=3.3V,IOVCC=3.3V,VSS=0V)
(DOUT) SDO		otherwise	noted, Ta=2	25°C,VCI	=3.3V,IOVCC=3.3V,VSS=0V)
(DOUT) SDO Item	Symbol	s otherwise Ra MIN	noted, Ta=2	25°C,VCI Unit	
(DOUT) SDO Item Chip Select Setup Time (Write)	Symbol TCSS	i otherwise Ra <u>MIN</u> 15	noted, Ta=2	25°C,VCI Unit	
(DOUT) SDO Item Chip Select Setup Time (Write) Chip Select Hold Time (Write)	Symbol TCSS TCSH	otherwise Ra MIN 15 15	noted, Ta=2	25°C,VCI Unit ns ns	
(DOUT) SDO Item Chip Select Setup Time (Write) Chip Select Hold Time (Write) Chip Select Setup Time (Read)	Symbol TCSS TCSH TCSS	s otherwise Ra MIN 15 15 60	noted, Ta=2	25°C,VCI Unit ns ns ns	
(DOUT) SDO Item Chip Select Setup Time (Write) Chip Select Hold Time (Write) Chip Select Setup Time (Read) Chip Select Hold Time (Read)	Symbol TCSS TCSH TCSS TSCC	otherwise Ra MIN 15 15 60 65	noted, Ta=2	25°C,VCI Unit ns ns ns ns	
(DOUT) SDO Item Chip Select Setup Time (Write) Chip Select Hold Time (Write) Chip Select Setup Time (Read) Chip Select Hold Time (Read) Chip Select "H" Pulse Width (Write)	Symbol TCSS TCSH TCSS TSCC TCHW	otherwise Ra <u>MIN</u> 15 15 60 65 40	noted, Ta=2	25°C,VCI Unit ns ns ns ns ns	CSX
(DOUT) SDO Item Chip Select Setup Time (Write) Chip Select Hold Time (Write) Chip Select Setup Time (Read) Chip Select Hold Time (Read) Chip Select "H" Pulse Width (Write) Serial Clock Cycle (Write)	Symbol TCSS TCSH TCSS TSCC TCHW TSCYCW TSCYCW TSHW TSLW	s otherwise Ra MIN 15 15 60 65 40 50	noted, Ta=2	25°C,VCI Unit ns ns ns ns ns ns ns	CSX
(DOUT) SDO Item Chip Select Setup Time (Write) Chip Select Hold Time (Write) Chip Select Setup Time (Read) Chip Select Hold Time (Read) Chip Select "H" Pulse Width (Write) Serial Clock "H" Pulse Width (Write) Serial Clock "L" Pulse Width (Write) Serial Clock "L" Pulse Width (Write) Serial Clock Cycle (Read)	Symbol TCSS TCSH TCSS TSCC TCHW TSCYCW TSHW	i otherwise Ra MIN 15 15 60 65 40 50 7 7 7 150	noted, Ta=2	25°C,VCI Unit ns ns ns ns ns ns ns ns	CSX
(DOUT) SDO Item Chip Select Setup Time (Write) Chip Select Hold Time (Write) Chip Select Setup Time (Read) Chip Select Hold Time (Read) Chip Select "H" Pulse Width (Write) Serial Clock Cycle (Write) Serial Clock "H" Pulse Width (Write) Serial Clock "L" Pulse Width (Write) Serial Clock "H" Pulse Width (Write) Serial Clock "H" Pulse Width (Read)	Symbol TCSS TCSH TCSS TSCC TCHW TSCYCW TSHW TSLW TSLW TSCYCR TSHR	s otherwise Ra MIN 15 15 60 65 40 50 7 7 7 150 60	noted, Ta=2	25°C,VCI Unit ns ns ns ns ns ns ns ns ns ns	CSX
(DOUT) SDO Item Chip Select Setup Time (Write) Chip Select Hold Time (Write) Chip Select Setup Time (Read) Chip Select Setup Time (Read) Chip Select "H" Pulse Width (Write) Serial Clock Cycle (Write) Serial Clock "H" Pulse Width (Write) Serial Clock "L" Pulse Width (Write) Serial Clock "H" Pulse Width (Read) Serial Clock "L" Pulse Width (Read) Serial Clock "L" Pulse Width (Read)	Symbol TCSS TCSH TCSS TSCC TCHW TSCYCW TSHW TSLW TSLW TSCYCR TSHR TSLR	s otherwise Ra MIN 15 15 60 65 40 50 7 7 7 150 60 60 60	noted, Ta=2	25°C,VCI Unit ns ns ns ns ns ns ns ns ns ns ns ns	DCX
(DOUT) SDO Item Chip Select Setup Time (Write) Chip Select Hold Time (Write) Chip Select Setup Time (Read) Chip Select Setup Time (Read) Chip Select "H" Pulse Width (Write) Serial Clock Cycle (Write) Serial Clock "H" Pulse Width (Write) Serial Clock "L" Pulse Width (Write) Serial Clock "H" Pulse Width (Read) Serial Clock "H" Pulse Width (Read) Serial Clock "L" Pulse Width (Read) D/CX Setup Time	Symbol TCSS TCSH TCSS TSCC TCHW TSCYCW TSHW TSCYCR TSLW TSCYCR TSHR TSLR TSLR TDCS	s otherwise Ra MIN 15 15 60 65 40 50 7 7 7 150 60 60 60 60 10	noted, Ta=2	25°C,VCI Unit ns ns ns ns ns ns ns ns ns ns ns ns ns	CSX
(DOUT) SDO Item Chip Select Setup Time (Write) Chip Select Hold Time (Write) Chip Select Hold Time (Read) Chip Select Hold Time (Read) Chip Select "H" Pulse Width (Write) Serial Clock Cycle (Write) Serial Clock "H" Pulse Width (Write) Serial Clock "L" Pulse Width (Write) Serial Clock "H" Pulse Width (Read) Serial Clock "L" Pulse Width (Read) Serial Clock "L" Pulse Width (Read) D/CX Setup Time D/CX Hold Time	Symbol TCSS TCSH TCSS TSCC TCHW TSCYCW TSHW TSLW TSLW TSLW TSCYCR TSHR TSLR TDCS TDCH	e otherwise Ra MIN 15 15 60 65 40 50 7 7 7 150 60 60 60 60 10 10	noted, Ta=2	25°C,VCI Unit ns ns ns ns ns ns ns ns ns ns ns ns ns	CSX DCX WRX
(DOUT) SDO Item Chip Select Setup Time (Write) Chip Select Hold Time (Write) Chip Select Hold Time (Write) Chip Select Setup Time (Read) Chip Select "H" Pulse Width (Write) Serial Clock Cycle (Write) Serial Clock "H" Pulse Width (Write) Serial Clock "L" Pulse Width (Write) Serial Clock "L" Pulse Width (Write) Serial Clock "L" Pulse Width (Read) Serial Clock "L" Pulse Width (Read) Serial Clock "L" Pulse Width (Read) D/CX Setup Time D/CX Hold Time Data Setup Time	Symbol TCSS TCSH TCSS TSCC TCHW TSCYCW TSHW TSLW TSLW TSLW TSCYCR TSHR TSLR TSLR TDCS TDCH TSDS	s otherwise Ra MIN 15 15 60 65 40 50 7 7 7 150 60 60 60 60 10	noted, Ta=2	25°C,VCI Unit ns ns ns ns ns ns ns ns ns ns ns ns ns	DCX
(DOUT) SDO Item Chip Select Setup Time (Write) Chip Select Hold Time (Write) Chip Select Setup Time (Read) Chip Select Setup Time (Read) Chip Select "H" Pulse Width (Write) Serial Clock Cycle (Write) Serial Clock "H" Pulse Width (Write) Serial Clock "L" Pulse Width (Write) Serial Clock "L" Pulse Width (Read) Serial Clock "L" Pulse Width (Read) Serial Clock "L" Pulse Width (Read) Serial Clock "L" Pulse Width (Read) D/CX Setup Time D/CX Hold Time Data Setup Time	Symbol TCSS TCSH TCSS TSCC TCHW TSCYCW TSHW TSLW TSLW TSLW TSLW TSLW TSLR TSLR TSLR TDCS TDCH TSDS TSDH	s otherwise Ra MIN 15 15 60 65 40 50 7 7 7 150 60 60 60 60 10 10 7 7 7	noted, Ta=2	25°C,VCI Unit ns ns ns ns ns ns ns ns ns ns ns ns ns	CSX DCX WRX SDA
(DOUT) SDO Item Chip Select Setup Time (Write) Chip Select Hold Time (Write) Chip Select Setup Time (Read) Chip Select Setup Time (Read) Chip Select "H" Pulse Width (Write) Serial Clock Cycle (Write) Serial Clock "H" Pulse Width (Write) Serial Clock "L" Pulse Width (Write) Serial Clock "L" Pulse Width (Write) Serial Clock "L" Pulse Width (Read) Serial Clock "L" Pulse Width (Read) Serial Clock "L" Pulse Width (Read) D/CX Setup Time D/CX Hold Time Data Setup Time Data Hold Time	Symbol TCSS TCSH TCSS TSCC TCHW TSCYCW TSHW TSLW TSLW TSLW TSCYCR TSHR TSLR TSLR TDCS TDCH TSDS	s otherwise Ra MIN 15 15 60 65 40 50 7 7 150 60 60 60 60 10 10 10 7	noted, Ta=2	25°C,VCI Unit ns ns ns ns ns ns ns ns ns ns ns ns ns	CSX DCX WRX
(DOUT) SDO Item Chip Select Setup Time (Write) Chip Select Hold Time (Write) Chip Select Setup Time (Read) Chip Select Setup Time (Read) Chip Select "H" Pulse Width (Write) Serial Clock Cycle (Write) Serial Clock "H" Pulse Width (Write) Serial Clock "L" Pulse Width (Write) Serial Clock "L" Pulse Width (Write) Serial Clock "L" Pulse Width (Read) Serial Clock "L" Pulse Width (Read) Serial Clock "L" Pulse Width (Read) Serial Clock "L" Pulse Width (Read) D/CX Setup Time D/CX Hold Time Data Setup Time Data Hold Time Access Time Output disable Time	Symbol TCSS TCSH TCSS TSCC TCHW TSCYCW TSHW TSLW TSLW TSLW TSLW TSLW TSLR TSLR TSLR TDCS TDCH TSDS TSDH	s otherwise Ra MIN 15 15 60 65 40 50 7 7 7 150 60 60 60 60 10 10 7 7 7	noted, Ta=2	25°C,VCI Unit ns ns ns ns ns ns ns ns ns ns ns ns ns	CSX DCX WRX SDA
(DOUT) SDO Item Chip Select Setup Time (Write) Chip Select Hold Time (Write) Chip Select Setup Time (Read) Chip Select Setup Time (Read) Chip Select "H" Pulse Width (Write) Serial Clock Cycle (Write) Serial Clock "H" Pulse Width (Write) Serial Clock "L" Pulse Width (Write) Serial Clock "L" Pulse Width (Write) Serial Clock "L" Pulse Width (Read) Serial Clock "L" Pulse Width (Read) Serial Clock "L" Pulse Width (Read) D/CX Setup Time D/CX Hold Time Data Setup Time Data Hold Time	Symbol TCSS TCSH TCSS TSCC TCHW TSCYCW TSHW TSCYCW TSHW TSCYCR TSHR TSLR TSLR TSLR TDCS TDCH TSDS TSDH TACC	s otherwise Ra MIN 15 15 60 65 40 50 7 7 7 150 60 60 60 60 10 10 10 7 7 10	noted, Ta=2 ting MAX	25°C,VCI Unit ns ns ns ns ns ns ns ns ns ns ns ns ns	CSX DCX WRX SDA

Note: All timing is defined as the reference to the 30-70% of IOVCC.

8. Interface

8.1 Interface

IM3	IM2	IM1	IM0	Interface
0	1	0	1	3-wire serial interface I
0	1	1	0	4-wire serial interface I
1	1	0	1	3-wire serial interface II
1	1	1	0	4-wire serial interface II

3-wire serial interface I

Pin Name	Description
CSX	Chip selection signal
DCX	Clock signal
SDA	Serial input/output data

4-wire serial interface I

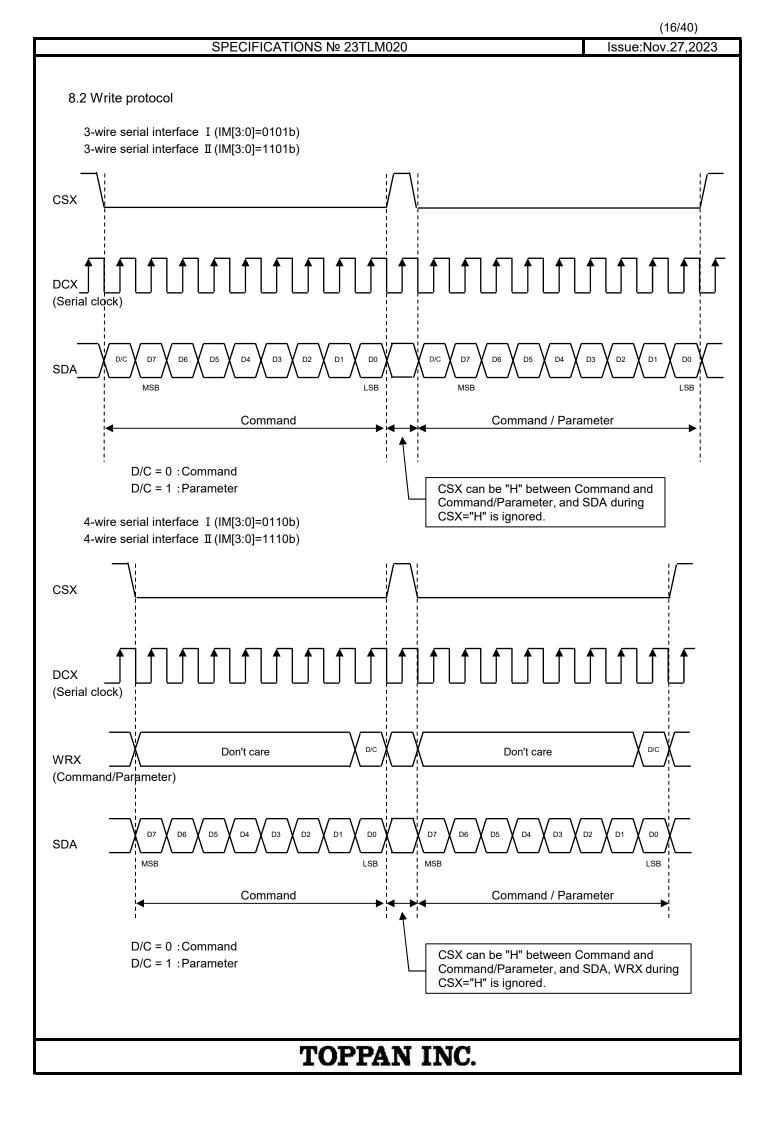
Pin Name	Description			
CSX	Chip selection signal			
WRX	WRX=Low :Command WRX=High: Parameter			
DCX	Clock signal			
SDA	Serial input/output data			

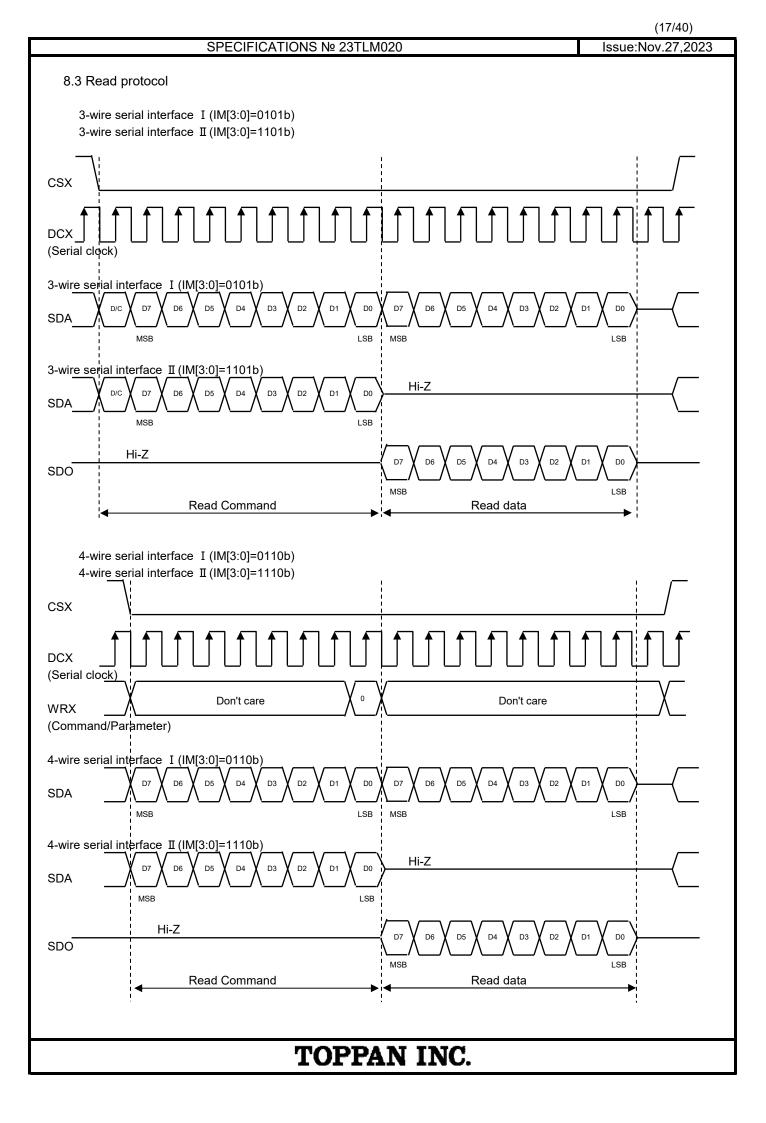
3-wire serial interface II

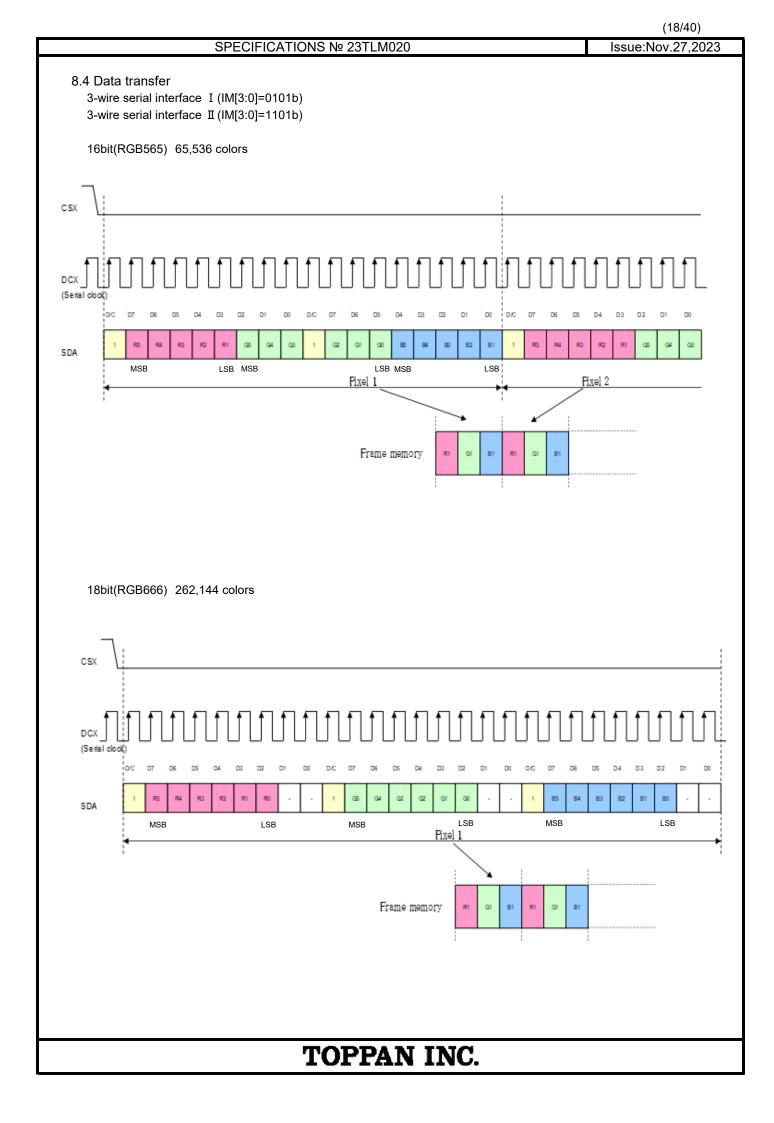
Pin Name	Description
CSX	Chip selection signal
DCX	Clock signal
SDA	Serial input data
SDO	Serial output data

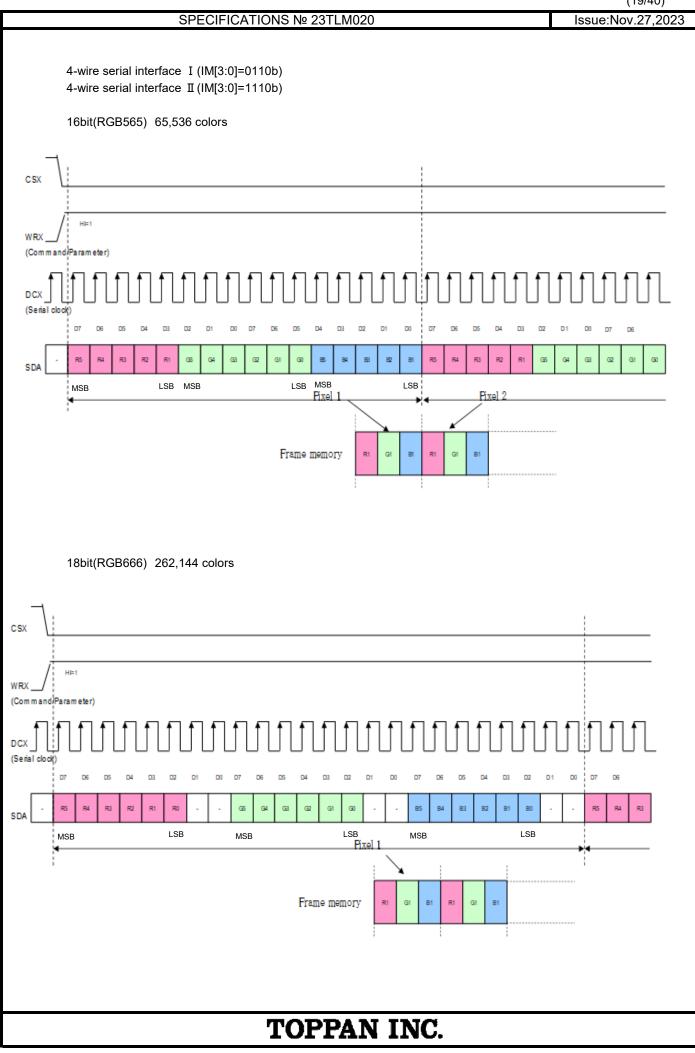
4-wire serial interface II

Pin Name	Description
CSX	Chip selection signal
WRX	WRX=Low :Command WRX=High : Parameter
DCX	Clock signal
SDA	Serial input data
SDO	Serial output data









(19/40)

TOPPAN INC.

0.			D/C	D[7:0]	Remarks
\square	VCI/IOV	CC ON			
\square	RESX High	RESX Low			RESX High can be omitted
$ \downarrow$	RESX High \rightarrow Low				
$ \rightarrow $	Wait 10 usec or more				
	RESX Lov	-			
_	Wait 120 ms				
1	Sleep		0	11h	
_	Wait 120 ms			0.01	
2	Memory acc		0	36h	
3	LCM C	para 1	1	00h	MX=MY=0
5	LOM C		0	C0h 3Ch	XINV=XMV=XMX=XBGR=1
1	Pixel f	para 1	0	3Ch 3Ah	XINV=XIIV=XIIX=XBGR=1
* -	Pixel I				DED-GEK DGD-262K
5	CMD	para 1	1 0	06h DFh	05h:65k,06h:262k
,	CMD			DFn 5Ah	
╞		para 1 para 2	1 1	5An 69h	
┝		para 2 para 3	1	09h	
╞		para 3 para 4	1	0211 01h	Command2 enable
3	GATEC		0	E4h	
ŕ	GAILC	para 1	1	27h	NL=320
ŀ		para 1 para 2	1	00h	SCN=G0
ŀ		para 2 para 3	1	10h	TMG=1,SM=GS=0
7	GATEC		0	B7h	1110-1,311-03-0
-	GAILC	para 1	0	75h	VGH=14.9,VGL=-10.4
3	VCOMS		0	BBh	VGH=14.3, VGE=10.4
ŕ	00000	para 1	1	20h	 Δv=0.9typ
)	VAP/VA		0	D2h	
Í		para 1	1	00h	
0	VRH		0	C3h	
- -		para 1	1	17h	VAP=4.7+
1	Frame		0	C6h	
ŀ		para 1	1	EFh	Column inversion,60Hz
2	Power c		0	D0h	
F		para 1	1	A4h	
ļ		para 2	1	A1h	
3	Positive		0	E0h	
ľ		para 1	1	A0h	
ļ		para 2	1	09h	
ļ		para 3	1	0Eh	
ļ		para 4	1	0Bh	
ļ		para 5	1	0Ch	
ľ		para 6	1	16h	
ſ		para 7	1	2Fh	
ļ		para 8	1	33h	
ľ		para 9	1	3Fh	
ſ		para 10	1	27h	
ľ		para 11	1	16h	
ľ		para 12	1	13h	
ļ		para 13	1	12h	
Ī		para 14	1	20h	
Ē	Wait 10 ms	ec or more			

9. Sequence

9.1 Power ON Sequence

SPECIFICATIONS № 23TLM020

	SPECIFICATIONS N	2 23 I LIVIU	20	Issue:Nov.27	,20
lo.		D/C	D[7:0]	Remarks	
14	Negative gamma	0	E1h		
	para 1	1	F0h		
	para 2	1	09h		
	para 3	1	0Fh		
	para 4	1	0Ch		
	para 5	1	0Ch		
	para 6	1	17h		
	para 7	1	30h		
	para 8	1	43h		
	para 9	1	42h		
	para 10	1	2Ah		
	para 11	1	17h		
	para 12	1	14h		
	para 13	1	15h		
	para 14	1	23h		
	Wait 10 msec or more				
15	Equalize control	0	E9h		
	para 1	1	08h		
	para 2	1	08h		
10	para 3	1	00h		
16	RGB interface control	0	B1h		
	para 1	1	00h		
	para 2	1	04h		
47	para 3	1	14h		
17	RAM Control	0	B0h		
	para 1	1	00h	CPU interface	
40	para 2	1	E0h		
18	CA SET	0	2Ah	N0(45.0)	
	para 1	1	00h	XS[15:8]	
	para 2	1	00h	XS[7:0]	
	para 3	1	00h	XE[15:8]	
10	para 4	1	EFh	XE[7:0]	
19	RA SET	0	2Bh	V0[45:0]	
	para 1	1 1	00h	YS[15:8]	
	para 2 para 3	1	00h 01h	YS[7:0] YE[15:8]	
	para 3 para 4	1	3Fh	YE[7:0]	
20	GT ADJ	0	B8h		
20	para 1	1	2Ah	-	
	para 1 para 2	1	28h		
	para 2 para 3	1	14h		
	para 3 para 4	1	F5h		
21	Tearing Effect On	0	35h		
	para 1	1	00h	TEM = 0	
22	RAMWR	0	2Ch		
	data 1	1	**** h	write data	
	data 1	1	**** h	write data	
		•••			
	data n	1	**** h	write data	
	wait 10 msec or more	I			
23	Display ON	0	29 h		
	wait 10 msec or more	0	2311		
24	Backlight ON				
- '	Duolaigne Ora		1		

Issue:Nov.27,2023

9.2 Sleep IN Sequence

No.		D/C	D[7:0]	Remarks
1	Backlight OFF			
2	Display OFF	0	28 h	
	Wait 10 msec or more			
3	Sleep In	0	10 h	

9.3 Sleep OUT Sequence

No.		D/C	D[7:0]	Remarks
1	Sleep Out	0	11 h	
	Wait 120 msec or more			
2	Display ON	0	29 h	
	Wait 50 msec or more			
3	Backlight ON			

9.4 Power OFF Sequence

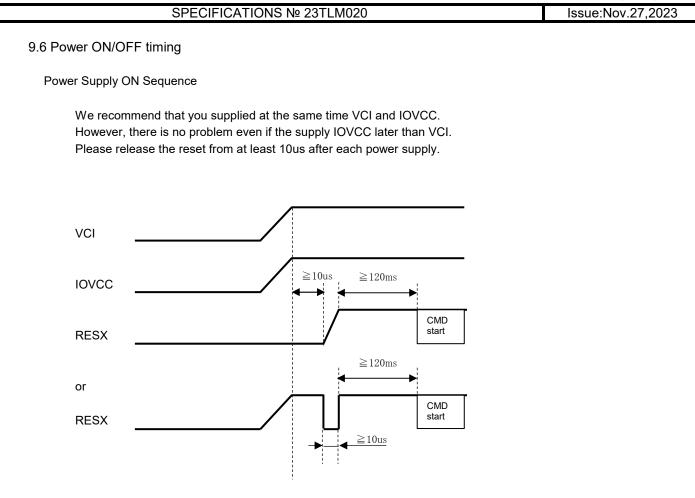
No.		D/C	D[7:0]	Remarks
1	Backlight OFF			
2	Display OFF	0	28 h	
	Wait 10 msec or more			
3	Sleep In	0	10 h	
	Wait 120 msec or more			
4	$RESX\:High\toLow$			
5	VCI/IOVCC OFF			

9.5 Refresh Sequence

A

No.		D/C	D[7:0]	(1/2) Remarks
1	Sleep Out	0	11h	
	Wait 120 msec or more			
2	Memory access control	0	36h	
	para 1	1	00h	MX=MY=0
3	LCM Control	0	C0h	
	para 1	1	3Ch	XINV=XMV=XMX=XBGR=1
4	Pixel format	0	3Ah	
	para 1	1	06h	05h:65k,06h:262k
5	CMD2EN	0	DFh	
	para 1	1	5Ah	
	para 2	1	69h	
	para 3	1	02h	
	para 4	1	01h	Command2 enable
6	GATECTRL 1	0	E4h	
	para 1	1	27h	NL=320
	para 2	1	00h	SCN=G0
	para 3	1	10h	TMG=1,SM=GS=0
7	GATECTRL 2	0	B7h	
	para 1	1	75h	VGH=14.9,VGL=-10.4
8	VCOMS setting	0	BBh	
	para 1	1	20h	∆v=0.9typ
9	VAP/VAN signal	0	D2h	
	para 1	1	00h	
10	VRH set	0	C3h	
	para 1	1	17h	VAP=4.7+
11	Frame rate	0	C6h	
	para 1	1	EFh	Column inversion,60Hz
12	Power control 1	0	D0h	
	para 1	1	A4h	
	para 2	1	A1h	
13	Positive gamma	0	E0h	
	para 1	1	A0h	
	para 2	1	09h	
	para 3	1	0Eh	
	para 4	1	0Bh	
	para 5	1	0Ch	
	para 6	1	16h	
	para 7	1	2Fh	
	para 8	1	33h	
	para 9	1	3Fh	
	para 10	1	27h	
	para 11	1	16h	
	para 12	1	13h	
	para 13	1	12h	
	para 14	1	20h	

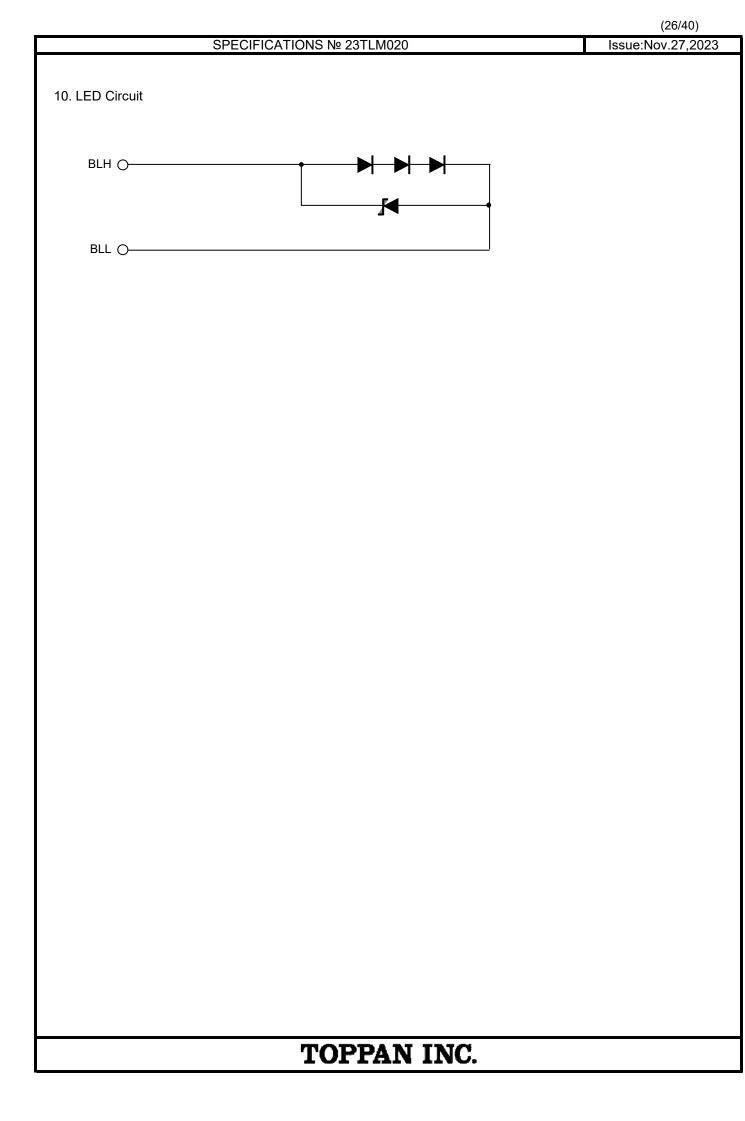
	SPECIFICATIONS N	№ 23TLM0	20		Issue:Nov.27,202
7					(2/2
No.		D/C	D[7:0]	Remarks	(2//
14	Negative gamma	0	E1h		
	para 1	1	F0h		
	para 2		09h		
	para 3	1	0Fh		
	para 4	1	0Ch		
	para 5	1	0Ch		
	para 6	1	17h		
	para 7	1	30h		
	para 8		43h		
	para 9		42h		
	para 10		2Ah		
	para 11		17h		
	para 12		14h		
	para 13		15h		
	para 14	1	23h		
	Wait 10 msec or more		=		
15	Equalize control	0	E9h		
	para 1		08h		
	para 2		08h		
16	para 3	1 0	00h B1h		
10	RGB interface control		00h		
	para 1 para 2		00h		
	para 2 para 3		14h		
17	RAM Control	0	B0h		
	para 1		00h	CPU interface	
	para 2		E0h	OF O Internace	
18	CA SET	0	2Ah		
	para 1	1	00h	XS[15:8]	
	para 2		00h	XS[7:0]	
	para 3		00h	XE[15:8]	
	para 4		EFh	XE[7:0]	
19	RA SET	0	2Bh		
-	para 1		00h	YS[15:8]	
	para 2		00h	YS[7:0]	
	para 3		01h	YE[15:8]	
	para 4	1	3Fh	YE[7:0]	
20	GT ADJ	0	B8h		
	para 1	1	2Ah		
	para 2	1	2Bh		
	para 3	1	14h		
	para 4		F5h		
21	Tearing Effect On	0	35h		
	para 1	1	00h	TEM = 0	
22	RAMWR	0	2Ch		
	data 1		**** h	write data	
	data 2		**** h	write data	
	••••	•••	••••h		
	data n	1	**** h	write data	
	wait 10 msec or more				
23	Display ON	0	29 h		
	wait 10 msec or more				



(25/40)

Power Supply OFF Sequence

We recommend that you removed at the same time VCI and IOVCC. However, there is no problem even if IOVCC OFF faster than VCI.



11. Characteristics

11.1 Optical Characteristics

(Measurement Condition)

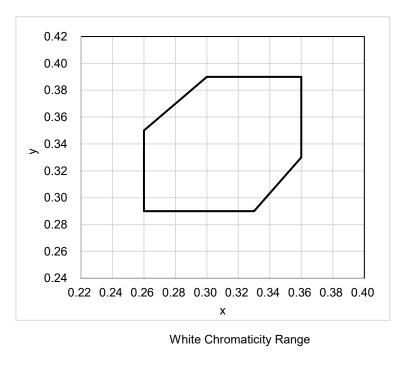
Measuring instruments: CS2000 (KONICA MINOLTA), LCD7200 (OTSUKA ELECTRONICS), EZcontrastXL88 (ELDIM) Driving condition: VCI=3.3V,IOVCC=3.3V, VSS=0V, Optimized VCOMDC

Backlight: IL= 7.0 mA

Measured temperature: Ta = 25°C

	Item	Symbol	Condition	MIN	TYP	MAX	Unit	Note №	Remark
Response time	Rise time + Fall time	TON + TOFF	[Data]= 00h← → 3Fh	-	-	100	ms	1	
Contrast ratio	Backlight ON	CR	[Data]= 3Fh / 00h	400	800	-		2	
Cont	Backlight OFF			-	2	-			
D	Left	θL	[Data]=	80	-	-	deg	3	
Viewing angle	Right	θR	3Fh / 00h	80	-	-	deg		
/iev an	Up	φU	CR ≧ 10	80	-	-	deg		
/	Down	φD		80	-	-	deg		
White	e Chromaticity	x y	[Data]= 3Fh	White chromaticity range		4			
Cente	er Brightness		[Data]= 3Fh	240	350	-	cd/㎡	5	
Brigh	tness distribution		[Data]= 3Fh	70	-	-	%	6	
Burn-in No noticeable burn-in image shall be observed after 2 hours of window pattern display.				•	7				

* Note number 1 to 7: Refer to the APPENDIX of "Reference Method for Measuring Optical Characteristics and Performance".



(White Chromaticity Range)

х	у
0.30	0.39
0.26	0.35
0.26	0.29
0.33	0.29
0.36	0.33
0.36	0.39

11.2 Temperature Characteristics

(Measurement Condition)

Measuring instruments: CS2000 (KONICA MINOLTA), LCD7200 (OTSUKA ELECTRONICS) Driving condition: VCI=3.3V,IOVCC=3.3V, VSS=0V, Optimized VCOMDC Backlight: IL= 7.0 mA

Ite	m	Symbol	Specif	ication	Remark
			Ta = -20 °C	Ta = 70 °C	
Response time	Rise time + Fall time	TON + TOFF	1000 msec or less	80 msec or less	
Contrast ratio	•	CR	200 or more	200 or more	Backlight ON
Display Quality			No noticeable display of should be observed.	lefect or ununiformity	

				(29/40)
		Issue:Nov.27,2023		
۵		of Judgment active Display and Screen Quality		
Ob	Drivir Signal o servation Illu	Condition: Observed TFT-LCD monitor from front during operation og Signal: Raster Patter (RGB, white, black) condition: [Data]:00h, 25h, 3Fh (3steps) distance: 30 cm minance: 200 to 350 lx Backlight: IL=7.0mA	n with the following co	nditions
Defe	ect item	Defect content	Criteria	
	_ine lefect	Black, white or color line, 3 or more neighboring defective dots	Not exists	
Display Quality	defect TFT or CF, or dust is counted as dot defect		Refer to table 1	
	Stain	Invisible through 5% ND filter at [Data]=00h	Acceptable	ND filter at Black screen

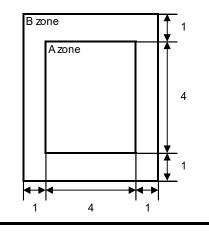
		Invisible through 5% N	D filter at [Data]=00h	Acceptable	
		Uneven brightness (wh	ite stain, black stain etc)	Invisible through 5% ND filter at Black screen. Invisible through 1% ND filter at other screen.	
lity	Foreign	Point-like	0.25mm< φ	N=0	
۵ua	particle		0.20mm< $\phi \leq 0.25$ mm	N≦2	
U L			$\phi \leq 0.20$ mm	Acceptable	
ree		Liner	3.0mm < L and 0.08mm < W	N=0	
Sc	Foreign particle		$L \leq 3.0mm$ or $W \leq 0.08mm$	Acceptable	
	Others			Use boundary sample	
				for judgment when necessary	

* φ (mm): Average diameter = (major axis + minor axis) / 2, W (mm): Width, L (mm): Length, N: Permissible number

Table1

Area	High bright dot	Low bright dot	Dark dot	Total	Criteria
A	0	2	2	3	Permissible distance between same color bright dots
					(includes neighboring dots): 3 mm or more
В	2	4	4	5	Permissible distance between same color high bright dots (includes neighboring dots): 5 mm or more
Total	2	4	4	5	

<Portrait model>



Division of A and B areas B area: Active area Dimensional ratio between A and B areas: 1: 4: 1 (Refer to the left figure)

12.2 Screen and Other Appearance

Testing conditions Observation distance: 30 cm Illuminance: 1200 \sim 2000 lx

	Item	Criteria	Remark
	Flaw	Ignore invisible defect when the backlight is on.	Applicable area: Active area only
Ľ	Stain		(Refer to the section 3.2 Outward Form)
riz€	Dirt		
ola	Stain Dirt Bubble Dust		
٩	Dust		
	Dent		
S	case	No functional defect occurs	
FF	PC	No functional defect occurs	

13. Reliability Test

A

	Test item	Test condition	number of failures /
			number of examinations
	High temperature storage	Ta = 80°C 240hrs	0/3
	Low temperature storage	Ta = -30°C 240hrs	0/3
	High temperature &	Ta = 60°C, RH = 90%, 240hrs	0/3
st	high humidity storage	non condensing 🛛 🛛 💥	
Durability test	High temperature operation	Tp = 70°C 240hrs	0/3
oilit	Low temperature operation	Tp = -20°C 240hrs	0/3
ırat	High temperature &	Tp = 40°C, RH = 90%, 240hrs	0/3
Ď	high humidity operation	non condensing 🛛 🛛 💥	
	Thermal shock storage	-30°C ↔ 80°C (30min / 30min) 100cycles	0/3
	Lightfastness	Xenon Blackpanel 63±3°C non-shower	0/3
		450W/㎡(300~700nm) non-operating Integral dose 800MJ/㎡	
	Electrostatic discharge test	Confirms to EIAJ ED-4701/300, C=200pF,R=0Ω,V=±200V	0/3
est	(Non operation)	Each 3 times of discharge on and power supply	
al te		and other terminals.	
Mechanical environmental test	Surface discharge test	C=250pF, R=100Ω, V=±12kV	0/3
шu	(Non operation)	Each 5 times of discharge in both polarities	
/irol		on the center of screen with the case grounded.	
env	Vibration test	Total amplitude 1.5mm, f=10 \sim 55Hz,	0/3
Sal		X,Y,Z directions for each 2 hours	
anic	Impact test	Use TOPPAN original jig (see next page) and	0/3
sch		make an impact with peak acceleration of 1000m/s ² for 6 msec	
Me		with half sine-curve at 3 times to each X, Y, Z directions	
		in conformance with JIS C 60068-2-27-2011.	
5	Packing vibration-proof test	Acceleration of 19.6m/s ² with frequency of $10 \rightarrow 55 \rightarrow 10$ Hz,	0 / 1 packing
Packing test		X,Y, Zdirection for each 30 minutes.	
ackin test	Packing drop test	Drop from 75cm high.	0 / 1 packing
۵		1 time to each 6 surfaces, 3 edges, 1 corner	

Note:Ta=ambient temperature Tp=Panel temperature

% The profile of high temperature/humidity storage and High Temperature/humidity operation (Pure water of over $10M\Omega \cdot cm$ shall be used.)

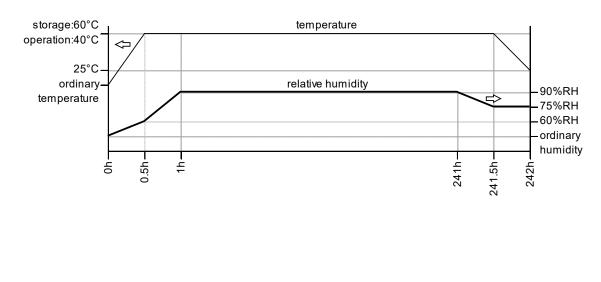
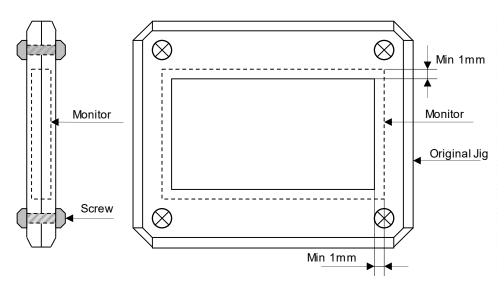


Table2. Reliability Criteria

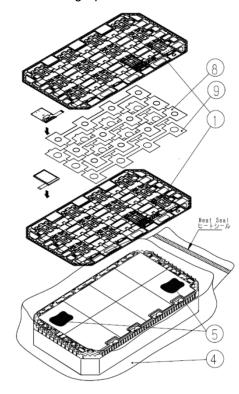
The parameters should be measured after leaving the monitor at the ordinary temperature for 24 hours or more after the test completion

or more alter th	e test completion.				
Item	Standard	Remark			
Display quality	No visible abnormality shall be seen.				
	(Except for unevenness by Pol deterioration.)				
Contrast ratio	200 or more	Backlight ON			

TOPPAN Original Jig



14. Packing Specifications



- Step 1. Each product is to be placed in one of the cut-outs of the tray with the display surface facing upward.
 Foam sheet A/B are to be placed on the products in the tray.
 Each product is to be placed in one of the cut-outs of the tray with the display surface facing downward.(24products per tray)
- Step 2. Each tray is to be piled up in same orientation and the trays be in a stack of 6.One empty tray is to be put on the top of stack of 6 trays.

 Step 3. 2 packs of moisture absorbers are to be placed on the top tray as shown in the drawing.
 Put piled trays into a sealing bag.

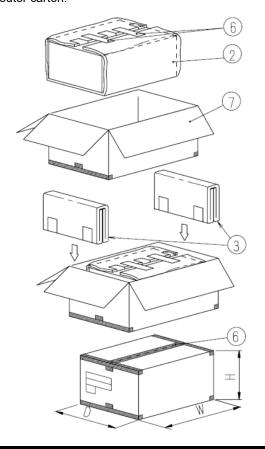
i ut plied trays into a sealing bag.

- Step 4. Vacuum and seal the sealing bag with the vacuum sealing machine.
- Step 5. The stack of trays in the plastic back is to be wrapped with B SHEET A.
- Step 6. The wrapped trays are placed in the carton.
- Step 7. B SHEET B are to be inserted into a outer carton with same orientation. The outer carton is to be sealed in H-shape with packing tape as shown in the drawing.
- Step 8. The model number, quantity of products, and shipping date are to be printed on the outer carton.If necessary, shipping labels or impression markings are to be put on the outer carton.

Remark: The return of packing materials is not required.

Packing item name		Specs., Material	
1	Tray	A-PET	
2	B SHEET A	Anti-static air bubble sheet	
3	B SHEET B	Anti-static air bubble sheet	
4	Sealing bag		
5	Drier	Moisture absorber	
6	Packing tape		
$\overline{\mathcal{O}}$	Outer carton	Corrugated cardboard	
8	FOAM SHEET A	Anti-static polyethylene	
9	FOAM SHEET B	Anti-static polyethylene	

Dimension of outer carton				
D : Approx.	(337mm)			
W : Approx.	(618mm)			
H : Approx.	(179mm)			
Quantity of products packed in one carton: 144				
Gross weight : Approx.	5.2 kg			



15. Handling Instruction

15.1 Cautions for Handling LCD panels

	Caution		
(1)	Do not make an impact on the LCD panel glass because it may break and you may get injured from it.		
(2)	If the glass breaks, do not touch it with bare hands. (Fragment of broken glass may stick you or you cut yourself on it.		
(3)	If you get injured, receive adequate first aid and consult a medial doctor.		
(4)	Do not let liquid crystal get into your mouth. (If the LCD panel glass breaks, try not let liquid crystal get into your mouth even toxic property of liquid crystal has not been confirmed.)		
(5)	If liquid crystal adheres, rinse it out thoroughly. (If liquid crystal adheres to your cloth or skin, wipe it off with rubbing alcohol or wash it thoroughly with soap. If liquid crystal gets into eyes, rinse it with clean water for at least 15 minutes and consult an eye doctor.		
(6)	If you scrap this products, follow a disposal standard of industrial waste that is legally valid in the community, country or territory where you reside.		
(7)	Do not connect or disconnect this product while its application products is powered on.		
(8)	Do not attempt to disassemble or modify this product as it is precision component.		
(9)	If a part of soldering part has been exposed, and avoid contact (short-circuit) with a metallic part of the case etc. about FPC of this model, please. Please insulate it with the insulating tape etc. if necessary. The defective operation is caused, and there is a possibility to generation of heat and the ignition.		
(10)	Since excess current protection circuit is not built in this TFT module, there is the possibility that LCD module or peripheral circuit become feverish and burned in case abnormal operation is generated. We recommend you to add excess current protection circuit to power supply.		
(11)	The devices on the FPC are damageable to electrostatic discharge, because the terminals of the devices are exposed. Wear grounded wrist-straps and use electrostatic neutralization blowers to prevent static charge and discharge when handling the TFT monitors. Designate an appropriate operating area, and set equipment, tools, and machines properly when handling this product.		

Caution



This mark is used to indicate a precaution or an instruction which, if not correctly observed, may result in bodily injury, or material damages alone.

15.2 Precautions for Handling

- Wear finger tips at incoming inspection and for handling the TFT monitors to keep display quality and keep the working area clean.
 Do not touch the surface of the monitor as it is easily scratched.
- 2) Wear grounded wrist-straps and use electrostatic neutralization blowers to prevent static charge and discharge when handling the TFT monitors as the LED in this TFT monitors is damageable to electrostatic discharge. Designate an appropriate operating area, and set equipment, tools, and machines properly when handling this product.
- Avoid strong mechanical shock including knocking, hitting or dropping to the TFT monitors for protecting their glass parts. Do not use the TFT monitors that have been experienced dropping or strong mechanical shock.
- 4) Do not use or storage the TFT monitors at high temperature and high humidity environment. Particularly, never use or storage the TFT monitors at a location where condensation builds up.
- 5) Avoid using and storing TFT monitors at a location where they are exposed to direct sunlight or ultraviolet rays to prevent the LCD panels from deterioration by ultraviolet rays.
- bo not stain or damage the contacts of the FPC cable .
 FPC cable needs to be inserted until it can reach to the end of connector slot.
 During insertion, make sure to keep the cable in a horizontal position to avoid an oblique insertion.
 Otherwise, it may cause poor contact or deteriorate reliability of the FPC cable.
- 7) The FPC cable is a design very weak to the bend and the pull as it is fixed with the tape. Do not bend or pull the FPC cable or carry the TFT monitor by holding the FPC cable.
- 8) Peel off the protective film on the TFT monitors during mounting process.
 Refer to the section 15.5 on how to peel off the protective film.
 We are not responsible for electrostatic discharge failures or other defects occur when peeling off the protective film.

15.3 Precautions for Operation

- Since this TFT monitors are not equipped with light shielding for the driver IC, do not expose the driver IC to strong lights during operation as it may cause functional failures.
- 2) In case of powering up or powering off this LCD module, be sure to comply the sequence as instructed in this specification.
- 3) Do not plug in or out the FPC cable while power supply is switch on. Plug the FPC cable in and out while power supply is switched off.
- 4) Do not operate the TFT monitors in the strong magnetic field. It may break the TFT monitors.
- 5) Do not display a fixed image on the screen for a long time.
 Use a screen-saver or other measures to avoid a fixed image displayed on the screen for a long time.
 Otherwise, it may cause burn-in image on the screen due the characteristics of liquid crystal.

15.4 Storage Condition for Shipping Cartons

(Storage environment)

Temperature	0 to 40° C
Humidity	60%RH or less
	No-condensing occurs under low temperature with high humidity condition.
Atmosphere	No poisonous gas that can erode electronic components and/or
	wiring materials should be detected.
 Time period 	1 year
 Unpacking 	To prevent damages caused by static electricity, anti-static precautionary measures
	(e.g. earthing, anti-static mat) should be implemented.
	After unpack, keep product in the appropriate condition,
	otherwise bubble seal of Protective film may be printed on Polarizer.
Maximum piling up	8 cartons (excluding the bottom)

*Conditions to storage after unpacking

(Storage environment)

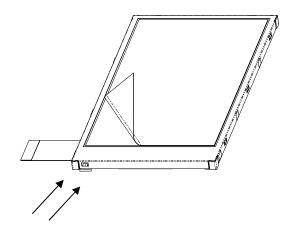
Temperature	0 to 40° C
Humidity	60%RH or less
	No-condensing occurs under low temperature with high humidity condition.
Atmosphere	No poisonous gas that can erode electronic components and/or
	wiring materials should be detected.
 Time period 	1 year (Shelf life)
Others	Keep/ store away from direct sunlight
	Storage goods on original tray made by TOPPAN.

15.5 Precautions for Peeling off the Protective film

The followings work environment and work method are recommended to prevent the TFT monitors from static damage or adhesion of dust when peeling off the protective films.

A) Work Environment

- a) Humidity: 50 to 70 %RH, Temperature15 to 27°C
- b) Operators should wear conductive shoes, conductive clothes, conductive finger tips and grounded wrist-straps. Use an electrostatic neutralization blower.
- c) Anti-static treatment should be implemented to work area's floor.Use a room shielded against outside dust with sticky floor mat laid at the entrance to eliminate dirt.
- B) Work Method
 - The following procedures should taken to prevent the driver ICs from charging and discharging.
 - a) Use an electrostatic neutralization blower to blow air on the TFT monitors to its lower left when FPC is placed at the left.
 Optimize direction of the blowing air and the distance between the TFT monitors and the electrostatic neutralization blower.
 - b) Put an adhesive tape (Scotch tape, etc) at the lower left corner area of the protective film to prevent scratch on surface of TFT monitors.
 - c) Peel off the adhesive tape slowly (spending more than 2 secs to complete) by pulling it to opposite direction.



Blower wind direction (Set an ion blower with its adequate conditions.)

15.6 Warranty

TOPPAN is only liable to defective goods which is stored and used under the condition complying with this specifications and returned within 1 (one) year. Warranty caused by manufacturing defect shall be conducted by replacement of goods or refundment at unit price.

APPENDIX

Reference Method for Measuring Optical Characteristics and Performance

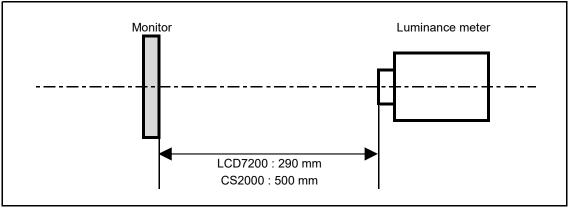
1. Measurement Condition

Measuring instruments: CS2000(KONICA MINOLTA), LCD7200(OTSUKA ELECTRONICS), EZcontrastXL88(ELDIM) Driving condition: Refer to the section "Optical Characteristics"

Measured temperature: 25°C unless specified

Measurement system: See the chart below. The luminance meter is placed on the normal line of measurement system. Measurement point: At the center of the screen unless otherwise specified

Dark box at constant temperature

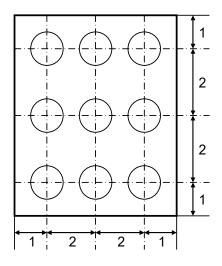


*Measurement is made after 30 minutes of lighting of the backlight.

Measurement point:

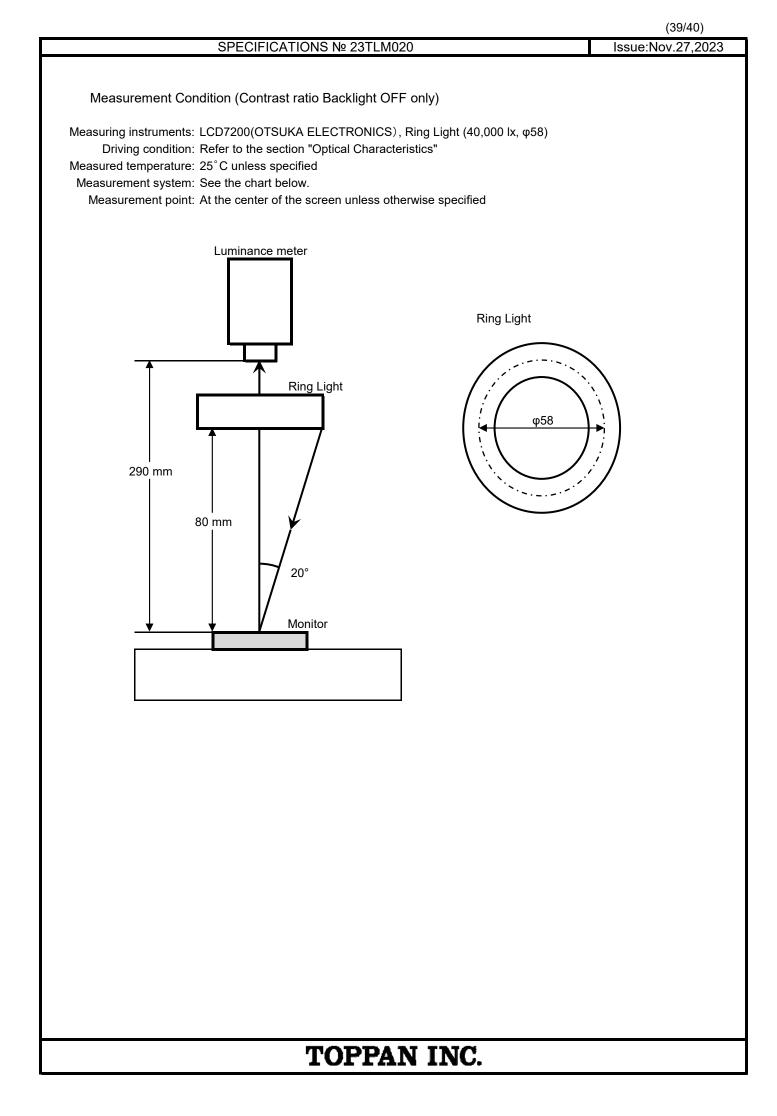
At the center point of the screen Brightness distribution: 9 points shown in the following drawing.

<Portrait model>



Dimensional ratio of active area

Backlight IL=7.0mA



lotice	Item	Test method	Measuring	Remark
	5		instrument	
1	Response	Measure output signal waveform by the luminance	LCD7200	Black display
	time	meter when raster of window pattern is changed from		[Data]=00h
		white to black and from black to white.		White display
		Black White Black		[Data]=3Fh
		400%		TON
		100%		Rise time
		90%		TOFF
				Fall time
2	Contrast ratio	Measure maximum luminance Y1([Data]=3Fh) and	CS2000	Backlight ON
		minimum luminance Y2([Data]=00h) at the center of	LCD7200	Backlight OFF
		the screen by displaying raster or window pattern.		
		Then calculate the ratio between these two values.		
		Contrast ratio = Y1/Y2		
		Diameter of measuring point: 7.8mmφ(CS2000)		
		Diameter of measuring point: 3 mmq(LCD7200)		
3	Viewing angle	Move the luminance meter from right to left and up	EZcontrastXL88	
		and down and determine the angles where		
	Horizontalθ	contrast ratio is 10.		
	Verticalφ			
4	White	Measure chromaticity coordinates x and y of CIE1931	CS2000	
	chromaticity	colorimetric system at [Data] = 3Fh		
		Color matching function: 2°view		
		measurement angle: 1°		
5	Center	Measure the brightness at the center of the screen.	CS2000	1
	brightness			
6	Brightness	(Brightness distribution) = 100 x B/A %	CS2000	1
	distribution	A : max. brightness of the 9 points	502000	
		B : min. brightness of the 9 points		
7	Burn-in	Visually check burn-in image on the screen		At optimized
•		after 2 hours of "window display" ([Data]=00h/3Fh).		VCOMDC





All good things come in threes:

With **Hardware**, **Software** and **Services**, we realise unique display solutions that turn your ideas into reality.



www.data-modul.com