



# SPECIFICATION



PH480272T016-ZHA 4.3" - WQVGA - RGB

Version: 1.1 Date: 14.05.2019

Note: This specification is subject to change without prior notice

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# History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
05/14/2019	01	001	New Drawing	-	Yuan
					al: 28 Page

Total: 28 Page

Page2



## Contents

## **1. SPECIFICATIONS**

- 1.1 Features
- 1.2 Mechanical Specifications
- 1.3 Absolute Maximum Ratings
- **1.4 DC Electrical Characteristics**
- **1.5 Optical Characteristics**
- 1.6 Backlight Characteristics

## 2. MODULE STRUCTURE

- 2.1 Counter Drawing
- 2.2 Interface Pin Description
- 2.3 Timing Characteristics

## **3. QUALITY ASSURANCE SYSTEM**

- 3.1 Quality Assurance Flow Chart
- 3.2 Inspection Specification

## **4. RELIABILITY TEST**

4.1 Reliability Test Condition

## **5. PRECAUTION RELATING PRODUCT HANDLING**

- 5.1 Safety
- 5.2 Handling
- 5.3 Storage
- 5.4 Terms of Warranty

Appendix : 1. LCM Drawing

Note: For detailed information please refer to IC data sheet: Sitronix--- SC7283-G4



# **1. SPECIFICATIONS**

## 1.1 Features

Item	Standard Value
Display Resolution	480 *3 (RGB) * 272 Dots
LCD Type	Full Viewing Angle , Normally Black , Transmissive type
Screen size(inch)	4.3 inch
Surface treatment	Anti-Glare
Color configuration	R.G.B. Vertical Stripe
Weight	- g
Interface	24 Bits RGB Interface
Driver IC	SC7283-G4
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website :
	http://www.powertip.com.tw/news_detail.php?Key=1&cID=1

## **1.2 Mechanical Specifications**

Item	Standard Value	Unit
Outline Dimension	105.5 (W) * 67.2 (L) * 2.6 (H)	mm

#### LCD panel

ltem	Standard Value	Unit	
Active Area	95.04 (W) * 53.856 (L)	mm	

Note : For detailed information please refer to LCM drawing.



## 1.3 Absolute Maximum Ratings

#### Module

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply for TFT Panel	Vdd	GND=0V	-0.3	4.0	V
Power Supply for Backlight Unit	Vcc	GND=0V	-0.3	+20.0	V
Operating Temperature	Тор	-	-20	+70	°C
Storage Temperature	Tst	-	-30	+80	°C

The absolute maximum rating values of this product are not allowed to be exceeded

at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

### 1.4 DC Electrical Characteristics

Module GND = 0V. Ta = 25 ℃ Item Symbol Condition Min. Max. Unit Typ. Power Supply for V GND=0V VDD 3.0 3.3 3.6 **TFT** Panel Power Supply for Vcc GND=0V 5 12 15 V **Backlight Unit** Vін GND=0V 0.7V<sub>DD</sub> VDD V Input Voltage for TFT \_ Panel V VIL GND=0V 0  $0.3V_{DD}$ \_ Supply Current for  $I_{DD}@V_{DD}=3.3V$ \_ \_ mΑ **TFT** Panel Supply Current for Icc@Vcc=5V (180)mΑ lcc \_ \_ **Backlight Unit** Supply Current for Icc@Vcc=12V mΑ (70) lcc \_ \_ **Backlight Unit** V **V**PH GND=0V 1.2 Input Voltage for **PWM Signal** V VPL GND=0V 0.4 \_ \_ fP GND=0V 5 **Dimming Clock Rate** 100 KHz \_



## **1.5 Optical Characteristics**

VDD=3.3V, Ta=25 ℃

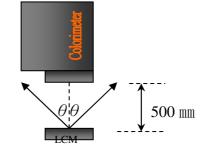
Item		Symbol	Condition	Min.	Тур.	Max.	unit	-
Response tin	ne	Tr + Tf	-	-	(30)	-	ms	Note2
	Тор	θ+		-	(80)	-		
Viewing angle	Bottom	θ-	CR ≥ 10	-	(80)	-	Dog	Note4
viewing angle	Left	θL		-	(80)	-	Deg.	NOLE4
	Right	θR		-	(80)	-		
Contrast rati	0	CR	-	(650)	(800)	-	-	Note3
	White	Х		-	(0.30)	-		
	vvnite	Y		-	(0.34)	-	- - -	
	Red	Х		-	(0.64)	-		
Color of CIE Coordinate	neu	Y	VCC=12V	-	(0.33)	-		
(LCD & BL)	Green	Х	PWM="High" (Duty=100%)	-	(0.31)	-		
		Y		-	(0.62)	-		
		Х		-	(0.14)	-		Note1
	Diue	Y		-	(0.05)	-		
Average Brightness Pattern=white display (LCD & BL) *1		IV	VCC=12V PWM="High"	-	(1000)	-	cd/m <sup>2</sup>	
Uniformity (LCD & BL)	*2	∆B	(Duty=100%)	70	-	-	%	



Note 1:

- \*1 : △B=B(min) / B(max) \* 100%
- \*2 : Measurement Condition for Optical Characteristics:
  - a : Environment: 25°C±5°C / 60±20%R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
  - b : Measurement Distance: 500 ± 50 mm  $\rightarrow$  ( $\theta$ = 0 °)
  - c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.
  - d : The uncertainty of the C.I.E coordinate measurement  $\pm 0.01$  , Average Brightness  $\pm 4\%$



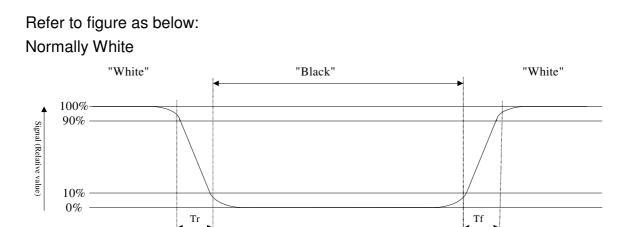


Colorimeter=BM-7 fast

To be measured at the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation (module)

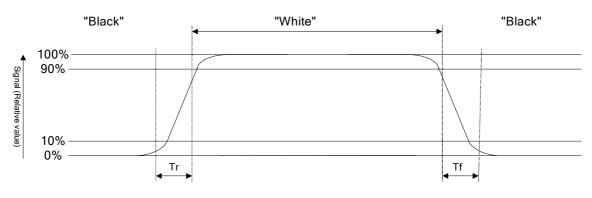
Note2: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white"(falling time) and from "white" to "black"(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of Amplitudes.





Normally Black



Note3: Definition of contrast ratio:

Contrast ratio is calculated with the following formula

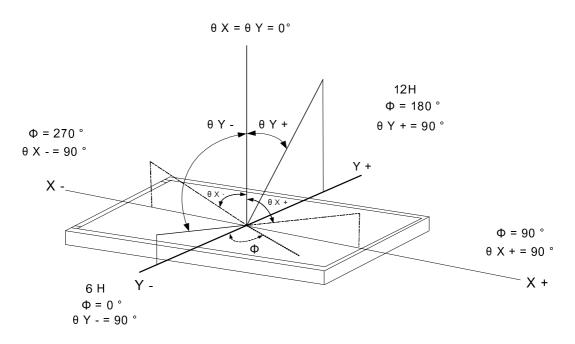
Photo detector output when LCD is at "White" state

Contrast ratio (CR) =

Photo detector output when LCD is at "Black" state

#### Note4: Definition of viewing angle:

Refer to figure as below:





### **1.6 Backlight Characteristics**

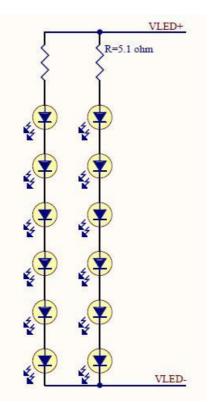
#### Maximum Ratings

Item	Symbol	Min. Max.		Unit	Remark	
LED Forward Current	lF	(30)		mA	One LED	
LED Reverse Voltage	VR	5		V		

Electrical / Optical Characteristics

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
LED Voltage	VL	-	(19.2)	-	V	Note1
LED Current	١L	-	40	-	mA	-
LED life time	-	50000		-	HR	Note2

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25 ℃ and I<sub>L</sub> =40 mA. The LED life time could be decreased if operating I<sub>L</sub> is larger than 40 mA.





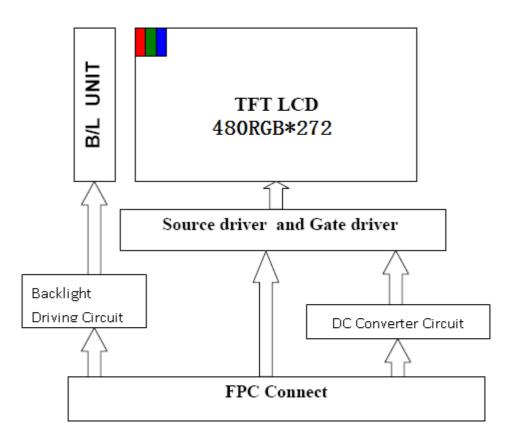
## 2. MODULE STRUCTURE

#### 2.1 Counter Drawing

#### 2.1.1 LCM Mechanical Diagram

\* See Appendix

#### 2.1.2 Block Diagram





## 2.2 Interface Pin Description

#### **TFT LCM Interface**

Pin#	Name	DESCRIPTION
1	GND	Power ground.
2	VDD	Power for Digital Circuit.
3	VDD	Power for Digital Circuit.
4	VCC	Power For LED backlight.
5	VCC	Power For LED backlight.
6	PWM	Shutdown & Dimming control input for backlight. Do not allow this pin to float. "Hi" =100%, "Low" = 0%.
7	GND	Power ground.
8	R0	Red Data.
9	R1	Red Data.
10	R2	Red Data.
11	R3	Red Data.
12	GND	Power ground.
13	R4	Red Data.
14	R5	Red Data.
15	R6	Red Data.
16	R7	Red Data.
17	GND	Power ground.
18	G0	Green Data.
19	G1	Green Data.
20	G2	Green Data.
21	G3	Green Data.
22	GND	Power ground.
23	G4	Green Data.
24	G5	Green Data.
25	G6	Green Data.
26	G7	Green Data.
27	GND	Power ground.
28	B0	Blue Data.
29	B1	Blue Data.

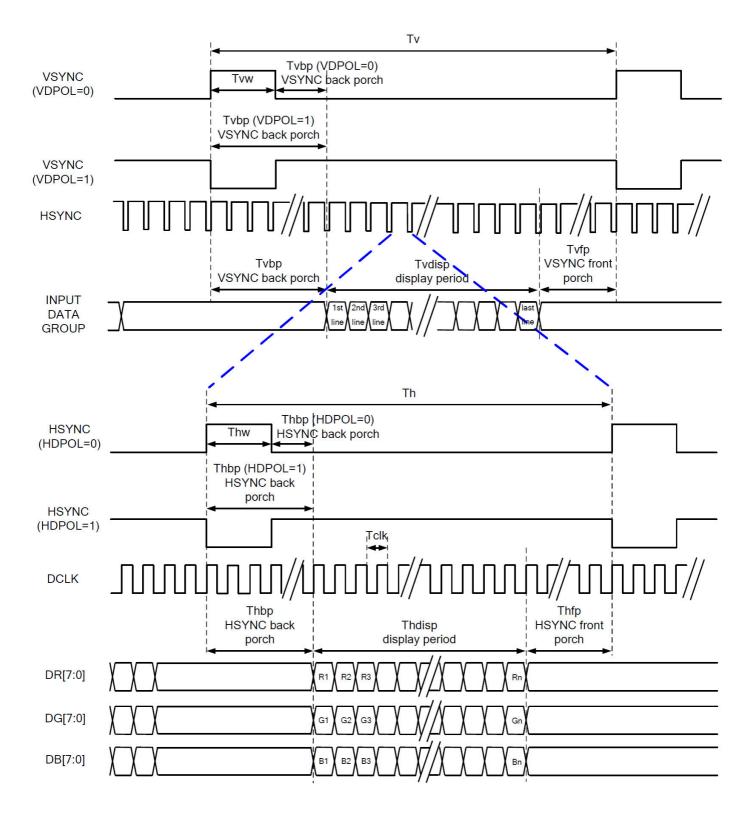


Pin#	Name	DESCRIPTION
30	B2	Blue Data.
31	B3	Blue Data.
32	GND	Power ground.
33	B4	Blue Data.
34	B5	Blue Data.
35	B6	Blue Data.
36	B7	Blue Data.
37	GND	Power ground.
38	HS	Line synchronization signal. Horizontal Sync Input.
39	VS	Frame synchronization signal. Vertical Sync Input.
40	GND	Power ground.
41	DE	Display enable pin from controller. Data Input Enable.
42	GND	Power ground.
43	DCLK	Sample clock. Data will be latched at the falling edge of DCLK.
44	GND	Power ground.
45	CS(NC) / ID1	No Function./ ID[4:1]These pins select LCM type.
46	SDIN(NC) / ID2	No Function./ ID[4:1]These pins select LCM type.
47	SCK(NC) / ID3	No Function ./ ID[4:1]These pins select LCM type.
48	DISPLAY CONTROL / ID4	Display Enable(Hi Active)./ ID[4:1]These pins select LCM type.
49	/RESET	Global Reset (Low Active).
50	GND	Power ground.



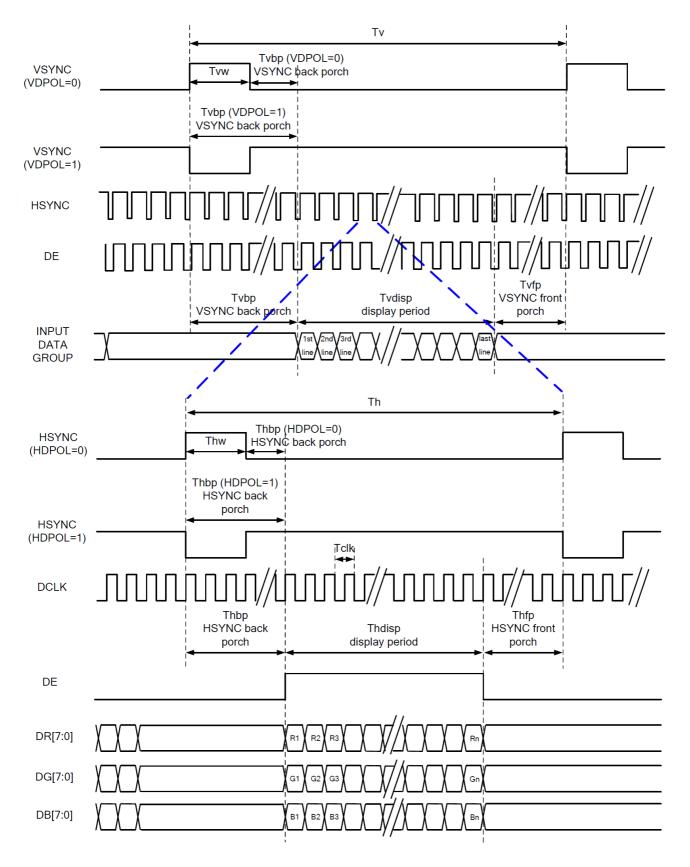
## 2.3 Timing Characteristics

#### 2.3.1 SYNC Mode



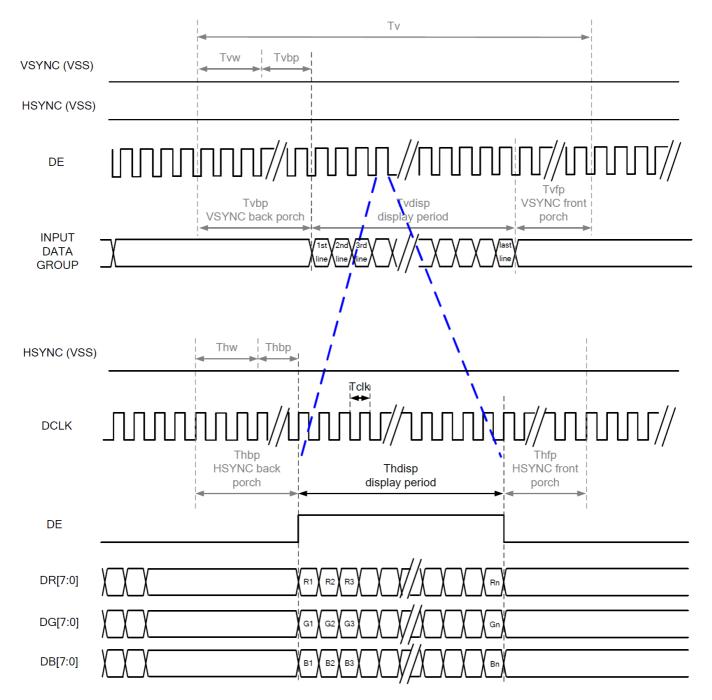


#### 2.3.2 SYNC-DE Mode





#### 2.3.3 DE Mode



RGB Mode Selection Table	DCLK	HSYNC	VSYNC	DE
SYNC - DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

Note: "Input" means these signals are driven by host side.

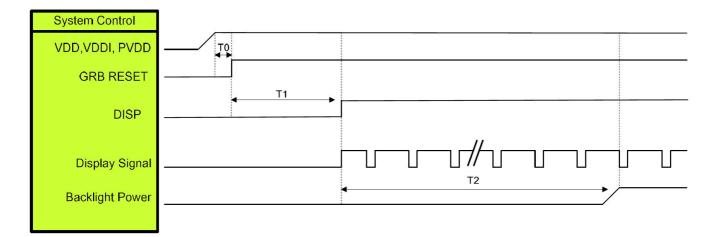


#### 2.3.4 Parallel 24-bit RGB Input Timing

	480RGB X 272 Resolution Timing Table								
ltem		Symbol	Min.	Тур.	Max.	Unit	Remark		
DCLK	Frequency	Fclk	8	9	12	MHz			
DCI	LK Period	Tclk	83	111	125	ns			
	Period Time	Th	485	531	598	DCLK			
	Display Period	Thdisp		480		DCLK			
HSYNC	Back Porch	Thbp	3	43	43	DCLK	By H_BLANKING setting		
	Front Porch	Thfp	2	8	75	DCLK			
	Pulse Width	Thw	2	4	43	DCLK			
	Period Time	Τv	276	292	321	HSYNC			
	Display Period	Tvdisp		272		HSYNC			
VSYNC	Back Porch	Tvbp	2	12	12	HSYNC	By V_BLANKING setting		
	Front Porch	Tvfp	2	8	37	HSYNC			
	Pulse Width	Tvw	2	4	12	HSYNC			

Note: It is necessary to keep Tvbp =12 and Thbp =43 in sync mode. DE mode is unnecessary to keep it.

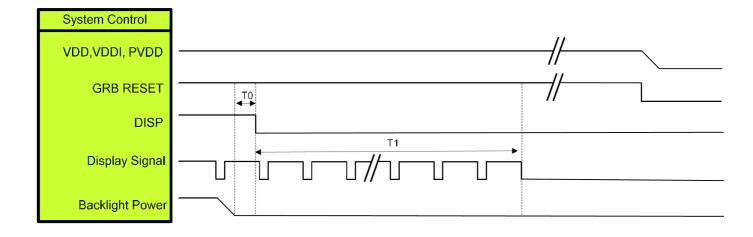
#### 2.3.5 Power ON Sequence



Symbol	Description	Min. Time	Unit
ТО	System power stability to GRB RESET signal	0	ms
T1	GRB RESET= "High" to DISP="High"	10	ms
T2	Display Signal output to Backlight Power on	250	ms



#### 2.3.6 Power Off Sequence

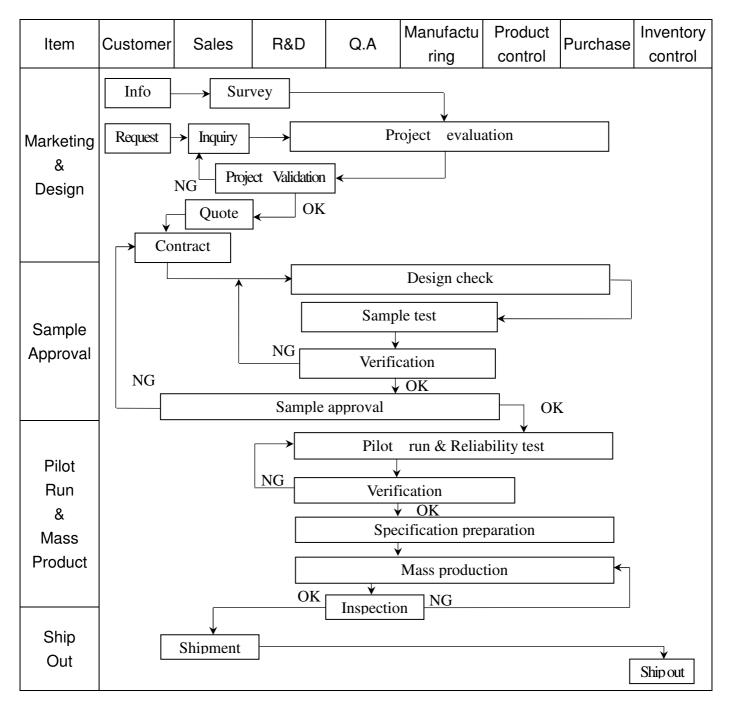


Symbol	Description	Min. Time	Unit
Т0	Backlight Power off to DISP="Low"	5	ms
T1	DISP="Low" to IC internal voltage discharge complete	80	ms



# **3. QUALITY ASSURANCE SYSTEM**

## 3.1 Quality Assurance Flow Chart



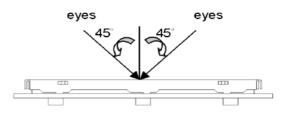


Item	Customer	Sales	R&D	Q.A	Manufact uring	Product control	Purchase	Inventory control
Sales Service	Info	Claim sis report	[	Trackin	Failure an Corrective	-		
Q.A Activity	1. ISO 900 3. Equipme 5. Standare	ent calibrati	ion	4	Process in . Education			es

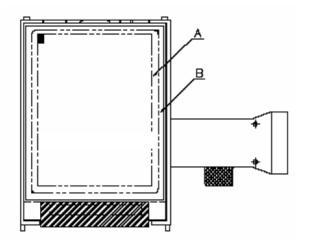
# **POWERTIP**

#### **3.2. Inspection Specification**

- Scope : The document shall be applied to TFT-LCD Module for 3. 5" ~10" (Ver.B01).
- ◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- ◆Equipment : Gauge、MIL-STD、Powertip Tester、Sample
- ◆Defect Level : Major Defect AQL : 0.4 ; Minor Defect AQL : 1.5
- ♦OUT Going Defect Level : Sampling.
- ◆Standard of the product appearance test :
  - a. Manner of appearance test :
  - (1). The test best be under 20W×2 fluorescent light, and distance of view must be at 30 cm.
  - (2). The test direction is base on about around  $45^{\circ}$  of vertical line.



#### (3). Definition of area.



A area : viewing area

**B** area : Outside of viewing area

(4). Standard of inspection : (Unit : mm)



#### ◆Specification For TFT-LCD Module 3. 5″~10″:

◆Specification For TFT-LCD Module 3. 5″~10″: (v						(Ver.B0	J1)
NO	Item		Criterion				
		-	1. 1The part number is inconsistent with work order of production.			Maj	jor
01	Product condition	1. 2 Mixe	d prod	uct types.		Maj	jor
		1. 3 Assen	nbled i	n inverse direction.		Maj	jor
02	Quantity	2. 1The q	uantity	y is inconsistent with	ı work order of production	Maj	jor
03	Outline dimension	3.1 Prod diagi		nension and struct	ure must conform to struc	ture Maj	jor
	Electrical Testing	4. 1 Missi	ing line	character and icon		Maj	jor
		4. 2 No function or no display.					jor
04		4. 3 Display malfunction.					jor
		4.4 LCD viewing angle defect.					jor
		4. 5 Current consumption exceeds product specifications.				Maj	jor
				Item	Acceptance (Q'ty)		
	Dot defect			<b>Bright Dot</b>	$\leq 4$		
	Dot defect		Dot	Dark Dot	≦ 5		
	(Bright dot 、	I	Defect	Joint Dot	≦ 3		
05	Dark dot)			Total	≦ 7	Mir	Minor
	On -display	5. 1 Inspection pattern : full white , full black , Red , Green and				and	
				blue screen	15.		
		5. 2 It is d	lefined	as dot defect if defe	ect area $>1/2$ dot.		
5. 3 The distance between two dot defect $\geq 5$ mm.							



<b>♦</b> Speci	fication For TFT-L	CD Module 3. 5'	′~10″:					(Ver.B01)
NO	Item		Criterion					
		6. 1 Round typ	e ( Non-display o	r display)	:			
		Dimensi	on (diameter : Ф	) — — —	ceptance			
	Black or white			A a	rea	B area		
	dot、scratch、		$\Phi \leq 0.25$	Ign	ore			
	contamination	0.25	$< \Phi \leq 0.50$	5	5	Ignor		
	Round type		$\Phi > 0.50$	0	)	Ignore		
			Total	5	;			
06	$\Phi = (x+y) / 2$ Line type	6. 2 Line type(	Non-display or d	isplay) :				Minor
		Leveth (L			Acceptance (Q'ty)			
		Line type	Length (L)	Length (L) Width (W)		A are	a Ba	irea
	⊂ / <sup>‡</sup> W		W	$\leq 0.03$	Ignor	e		
		L ≦10.0	0.03 < W	$\leq 0.05$	4			
		L ≦5.0	0.05 < W	$\leq 0.10$	2	Ign	ore	
			W	>0.10	As rou type			
			Total		5			
				Δ	ceptance	$(0^{\prime}\mathbf{t}\mathbf{v})$		
		Dimension	(diameter ∶ Φ)	A al		B ar	ea	
			$\Phi \leq 0.25$	Igno	ore	re		
07	Polarizer	0.25 <	$\Phi \leq 0.50$	4	4			Minor
	Bubble	0.50 <	$\Phi \leq 0.80$	1		Igno	re	
			$\Phi > 0.80$	0				
		,	Fotal	5				



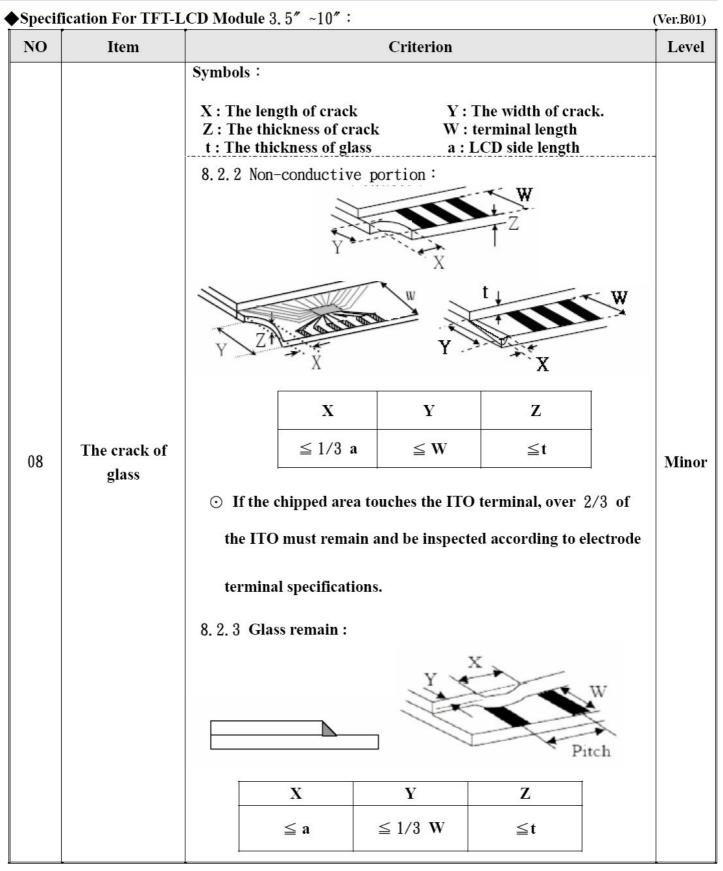
#### ◆Specification For TFT-LCD Module 3. 5″~10″:

◆Specification For TFT-LCD Module 3. 5″~10″: (Ver.)							
NO	Item	Criterion					
		Z : The thickness of crack V	Y : The width of crack. V : terminal length a : LCD side length				
		<ul> <li>8.1 General glass chip :</li> <li>8.1.1 Chip on panel surface and cra</li> </ul>	tick between panels: $Z \rightarrow Y$				
08	The crack of glass		ING	Minor			
		Seal width	Y				
		X Y	Z				
		≤ a Crack can't enter viewing area	$\leq 1/2 t$				
		$\leq a \qquad \begin{array}{c} Crack \ can't \ exceed \ the \\ half \ of \ SP \ width. \end{array}$	$1/2 t < Z \leq t$				



◆Specification For TFT-LCD Module 3. 5″~10″: (Ver.)							
NO	Item		Criterion				
		Symbols :Y : The width of crackX : The length of crackY : The width of crack.Z : The thickness of crackW : terminal lengtht : The thickness of glassa : LCD side length8. 1. 2 Corner crack : $X + Z = Z$					
		X	Y		Z		
		$\leq 1/5$ a	Crack can't en viewing area		$L \leq 1/2 t$		
			$\leq 1/5$ a	Crack can't excee half of SP widt		$t < Z \leq 2 t$	
08	The crack of glass				$\frac{Z}{\leq t}$	Minor	







#### ◆Specification For TFT-LCD Module 3. 5″~10″:

◆Specification For TFT-LCD Module 3. 5″~10″: (Ver.E							
NO	Item	Criterion	Level				
		9. 1 Backlight can't work normally.	Major				
09	Backlight elements	9. 2 Backlight doesn't light or color is wrong.	Major				
		9. 3 Illumination source flickers when lit.	Major				
	General appearance	10. 1 Pin type < quantity < dimension must match type in structure diagram.	Major				
		10. 2 No short circuits in components on PCB or FPC .	Major				
		10.3 Parts on PCB or FPC must be the same as on the production characteristic chart .There should be no wrong parts , missing parts or excess parts.	Major				
10		10. 4 Product packaging must the same as specified on packaging specification sheet.	Minor				
		10. 5 The folding and peeled off in polarizer are not acceptable.	Minor				
		10. 6 The PCB or FPC between B/L assembled distance(PCB or FPC ) is ≤1.5 mm.	Minor				



## 4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

4.1	Reliability lest Condition (ver.BUI)					
NO.	<b>TEST ITEM</b>		TEST CONDITION			
1	High Temperature Storage Test	Keep in 80°C ±5°	Keep in 80℃ ±5℃ 240 hrs			
2	Low Temperature Storage Test	Keep in − <b>30°</b> C ±5	°C 240 hrs			
3	High Temperature / High Humidity Storage Test	Keep in 60 °C / 9 (Excluding the po		n for 240 hrs		
4	Temperature Cycling	(30mi		$\rightarrow 80^{\circ}C \rightarrow +25^{\circ}C$ (30mins) (5mins)		
	Storage Test	-	<b>20</b> C	ycle		
		Air Discharge:		Contact Discharge:		
	ESD Test	Apply 2 KV with	5 times	Apply 250 V with 5 times		
		Discharge for eac	h polarity +/-	discharge for each polarity +/-		
		1. Temperature ambiance : 15°C ~ 35°C				
5		2. Humidity relative : $30\% \sim 60\%$				
Э		3. Energy Storage Capacitance(Cs+Cd) : 150pF±10%				
		4. Discharge Resistance(Rd) : 330Ω±10%				
		5. Discharge, mode of operation :				
		Single Discharge (time between successive discharges at least 1 sec)				
		(Tolerance if the output voltage indication : ±5%)				
		1. Sine wave 10~	55 Hz frequency	(1 min/sweep)		
6	Vibration Test (Packaged)	2. The amplitude	of vibration :1.5	mm		
	(I achagea)	3. Each direction	$(\mathbf{X} \cdot \mathbf{Y} \cdot \mathbf{Z})$ durat	ion for <b>2</b> Hrs		
		Pac	king Weight (Kg)	Drop Height (cm)		
			0 ~ 45.4	122		
	Drop Test		45.4 ~ 90.8	76		
7	(Packaged)		90.8 ~ 454	61		
	-		Over 454	46		
		Drop Direction : ※1 corner / 3 edges / 6 sides each 1 time				

 $\bigcirc$  Result Evaluation Criteria :

Under the display quality test conditions with normal operations with normal operation state. Do not change these conditions as such changes may affect practical display function. (Normal operation state)

Temperature : +20~30℃ Humidity : 50~70% Atmospheric pressure : 86~106Kpa



## **5. PRECAUTION RELATING PRODUCT HANDLING**

## 5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

## 5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is  $320\pm10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

## 5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is  $25^{\circ}C \pm 5^{\circ}C$  and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

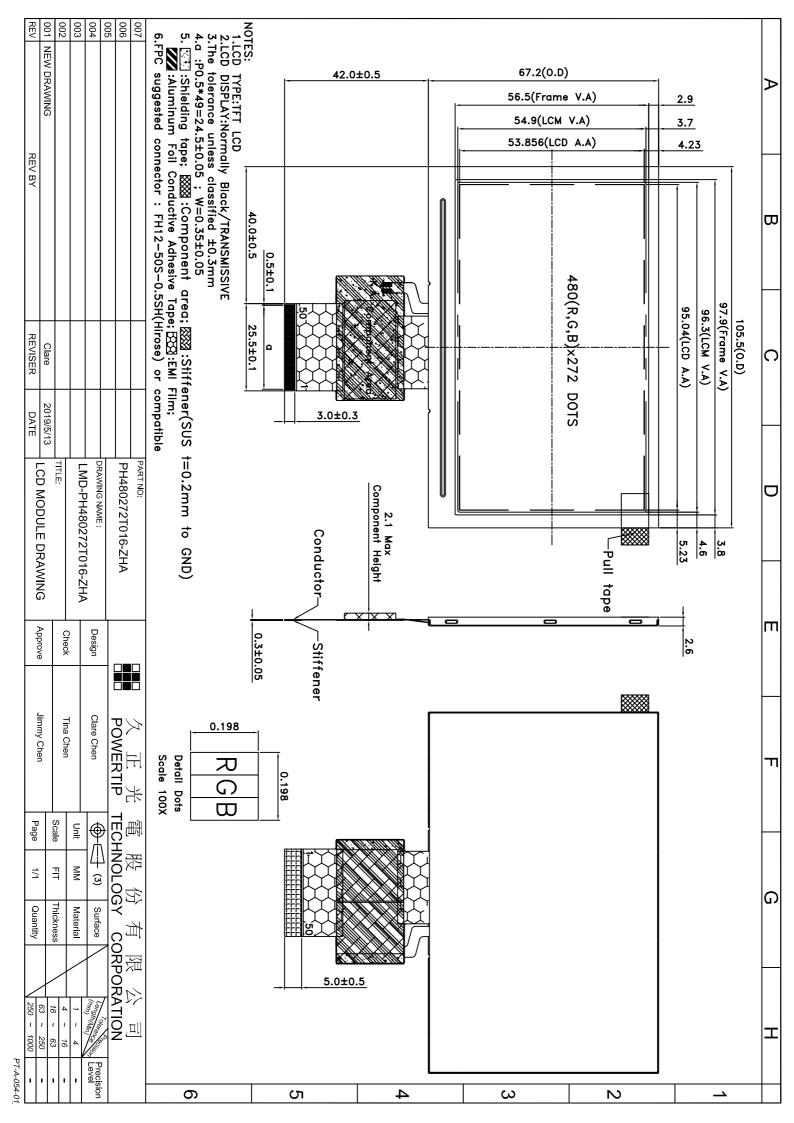
## **5.4 TERMS OF WARRANTY**

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



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